

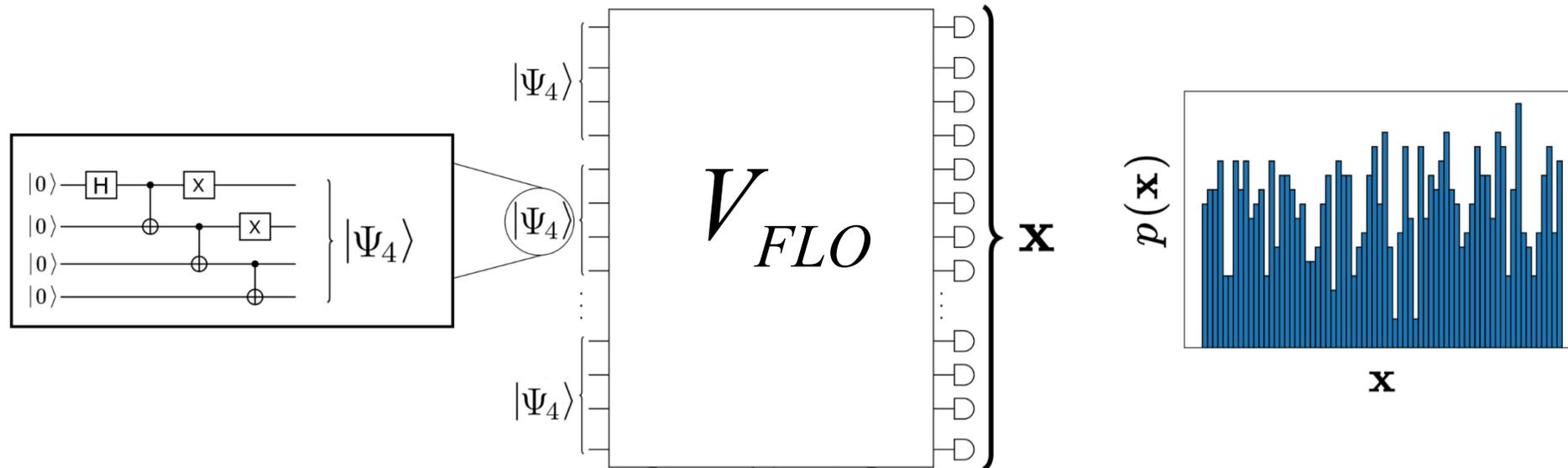
Fermion Sampling: a robust quantum advantage scheme using fermionic linear optics and magic input states

Michał Oszmaniec, Ninnat Dangniam, Mauro Morales, Zoltán Zimborás

[arxiv:2012.15825](https://arxiv.org/abs/2012.15825)

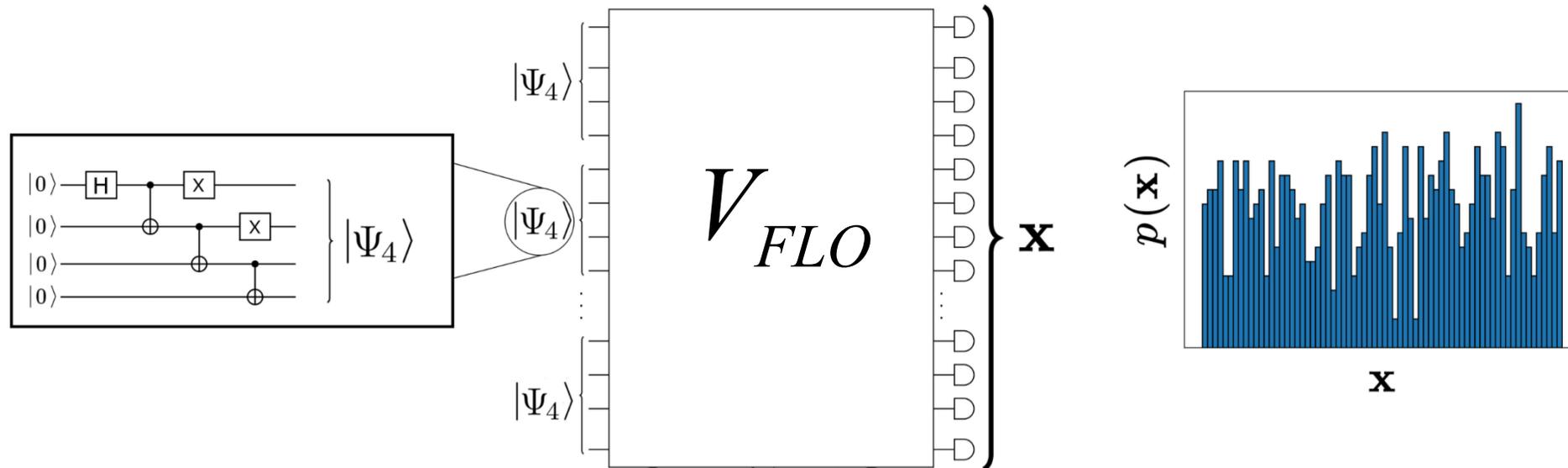


Fermion Sampling with magic input states



Proposal for quantum computational advantage/supremacy: sample random FLO circuits

Fermion Sampling with magic input states



Proposal for quantum computational advantage/supremacy: sample random FLO circuits

- Fermionic analogue of Boson Sampling
- Feasible in near-term architectures
- Hardness guarantees matching Random Circuit Sampling

Near-term quantum computers

- Present-day quantum computers are noisy, imperfect and not scalable.
- Implementation of complicated quantum algorithms (like Shor algorithm) in the near-term is **science-fiction**

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How to factor 2048 bit RSA integers in 8 hours using 20 million noisy qubits

Craig Gidney^{1,*} and Martin Ekerå²

¹Google Inc., Santa Barbara, California 93117, USA

²KTH Royal Institute of Technology, SE-100 44 Stockholm, Sweden
Swedish NCSA, Swedish Armed Forces, SE-107 85 Stockholm, Sweden

(Dated: December 6, 2019)

Historical cost estimate at $n = 2048$	Physical assumptions				Approach		Estimated costs		
	Physical gate error rate	Cycle time (microseconds)	Reaction time (microseconds)	Physical connectivity	Distillation strategy	Execution strategy	Physical qubits (millions)	Expected runtime (days)	Expected volume (megaqubitdays)
Fowler et al. 2012 [9]	0.1%	1	0.1	planar	1200 T	single threaded	1000	1.1	1100
O’Gorman et al. 2017 [18]	0.1%	10	1	arbitrary	block CCZ	single threaded	230	3.7	850
Gheorghiu et al. 2019 [19]	0.1%	0.2	0.1	planar	1100 T	single threaded	170	1	170
(ours) 2019 (1 factory)	0.1%	1	10	planar	1 CCZ	serial distillation	16	6	90
(ours) 2019 (1 thread)	0.1%	1	10	planar	14 CCZ	single threaded	19	0.36	6.6
(ours) 2019 (parallel)	0.1%	1	10	planar	28 CCZ	double threaded	20	0.31	5.9

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- Still, we hope that near-term quantum computers will be useful for **something** [Preskill, 2018]

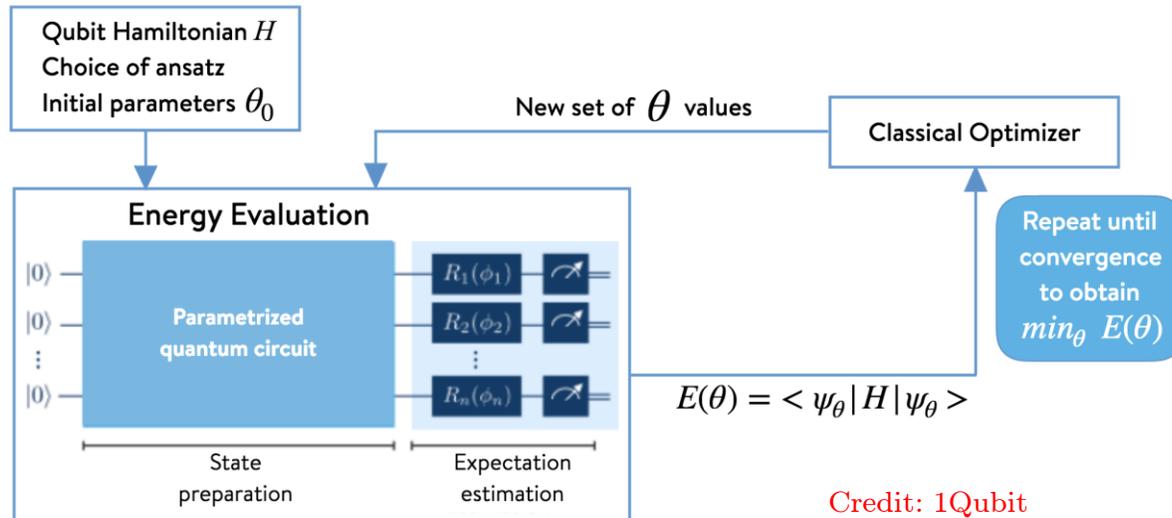


Near-term quantum computers (II)

- A popular approach: Variational Quantum Algorithms

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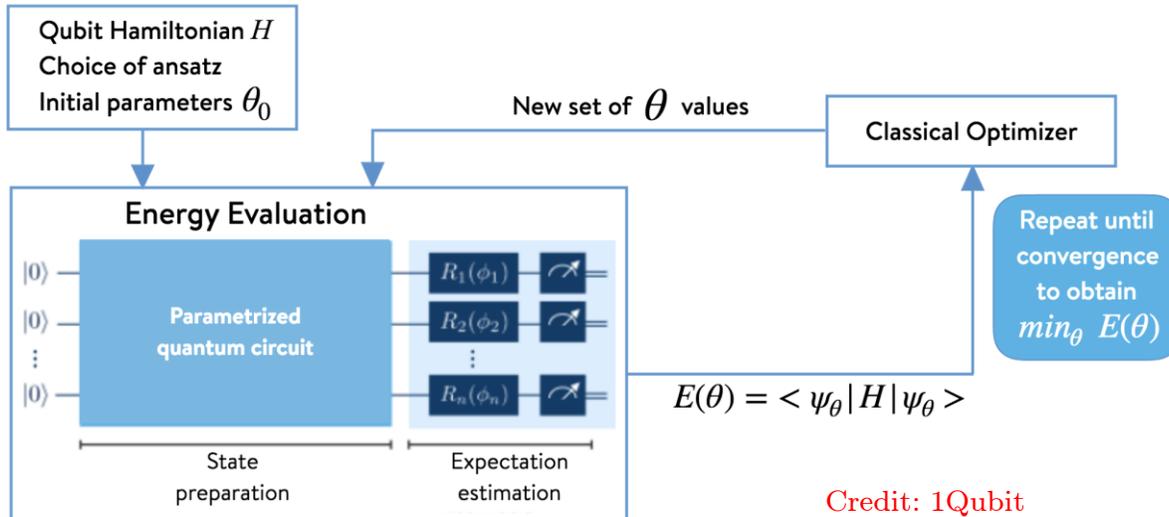
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Credit: 1Qubit

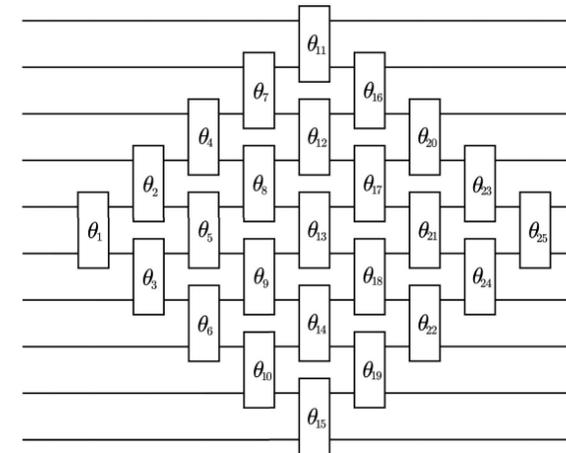
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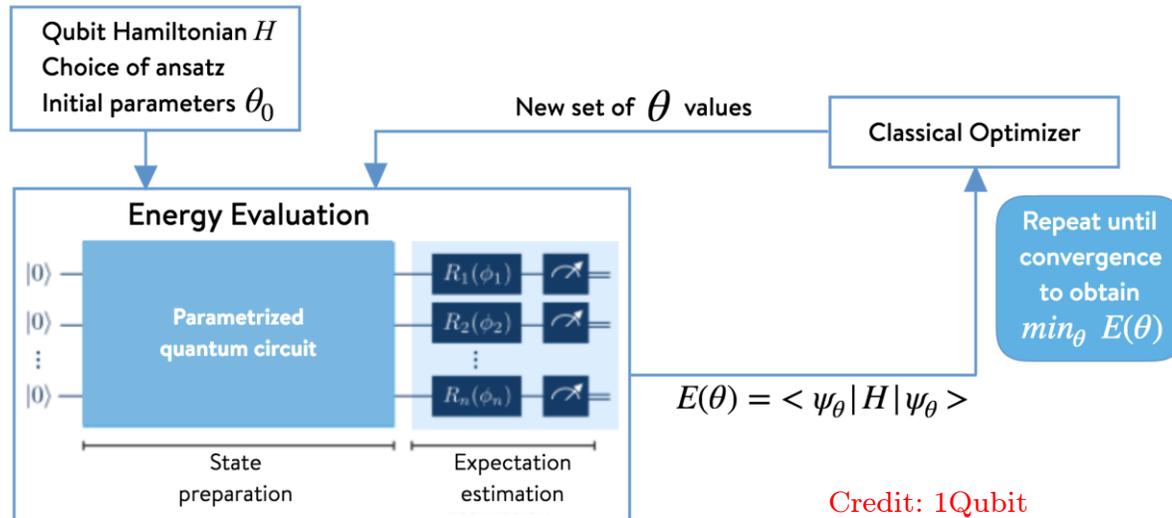
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Exemplary parametric circuit



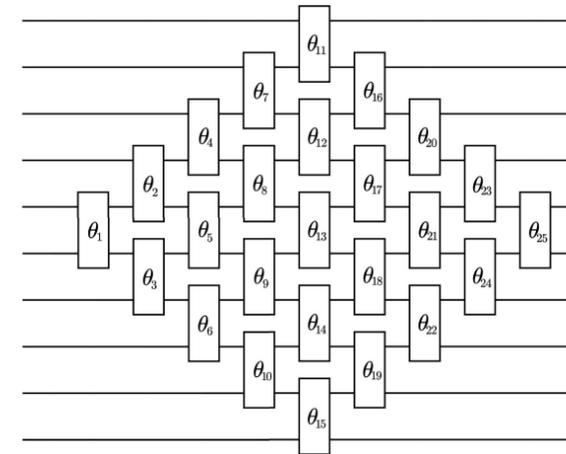
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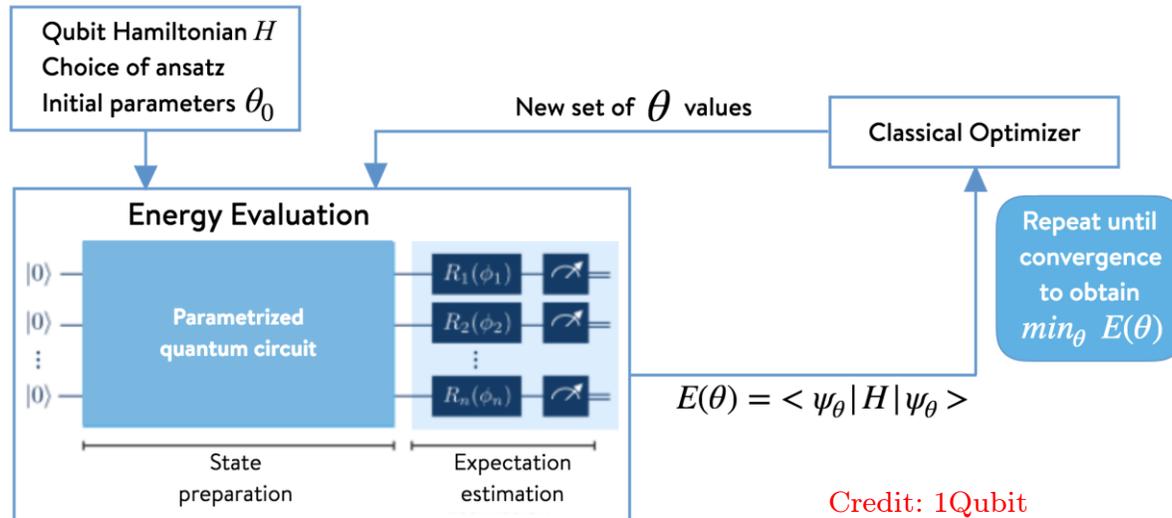
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- **Classical H:** combinatorial optimization problems (MAX-CUT, Spin glasses)

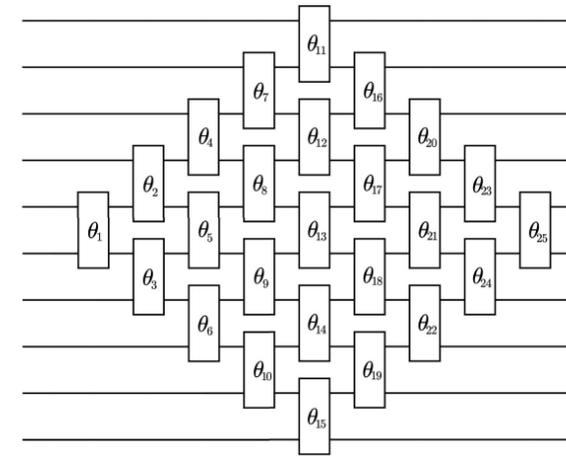
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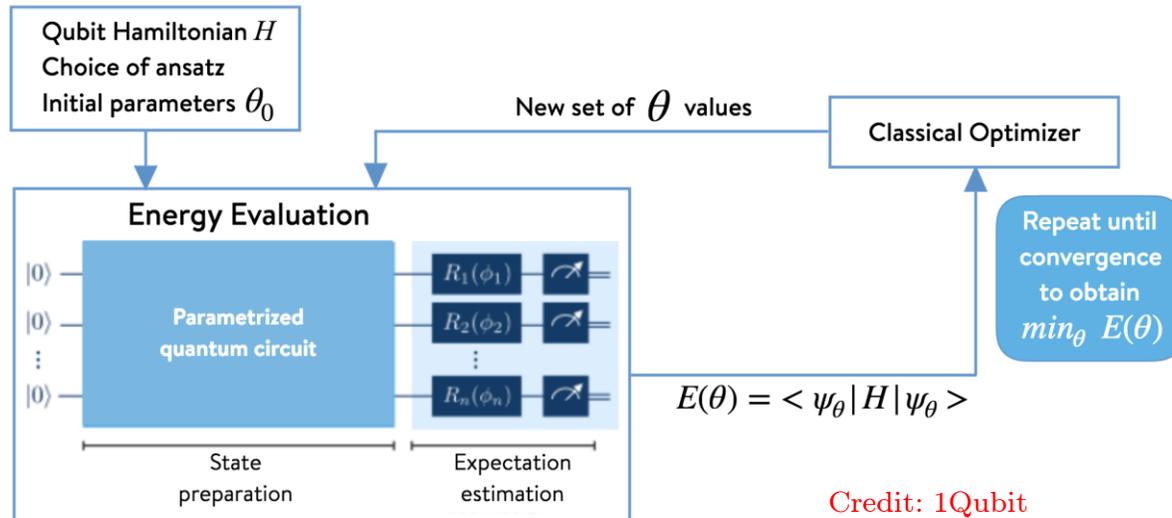
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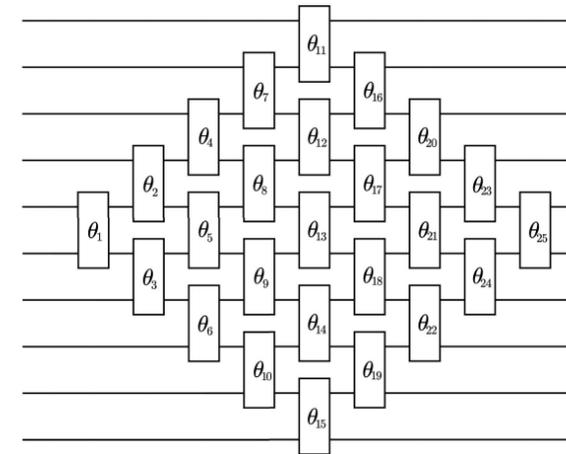
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- **Parametric circuits** will be useful in the near-term.

Near-term quantum computers (II)

- A popular approach: **Variational Quantum Algorithms**

Quantum Approximate Optimization of Non-Planar Graph Problems on a Planar Superconducting Processor

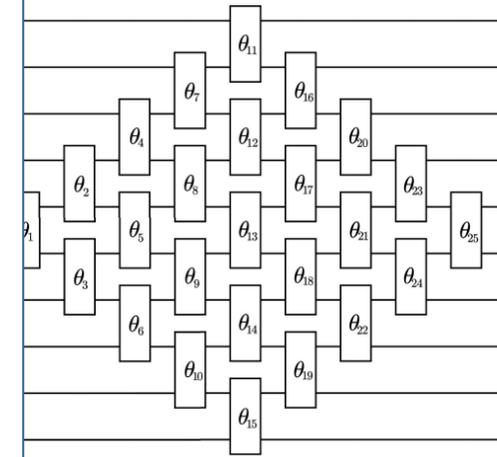
Google AI Quantum and Collaborators*
(Dated: April 10, 2020)

We demonstrate the application of the Google Sycamore superconducting qubit quantum processor to discrete optimization problems with the quantum approximate optimization algorithm (QAOA). Like past QAOA experiments, we study performance for problems defined on the connectivity graph of our hardware; however, we also apply the QAOA to the Sherrington-Kirkpatrick model and 3-regular MaxCut, both high dimensional graph problems requiring significant compilation. Experimental scans of the QAOA energy landscape show good agreement with theory across even the largest instances studied (23 qubits) and we are able to perform variational optimization successfully. For problems defined on the planar graph of our hardware we obtain an approximation ratio that is independent of problem size and observe, for the first time, that performance increases with circuit depth. For problems requiring compilation, performance decreases with problem size but still provides an advantage over random guessing for circuits involving several thousand gates.

- This behavior highlights the challenge of using near-term quantum computers to optimize problems on graphs differing from hardware connectivity. As these graphs are more representative of real world instances, our results advocate for more emphasis on such problems in the developing tradition of using the QAOA as a holistic benchmark of quantum processors.

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Exemplary parametric circuit



Γ , Spin glasses)

Quantum chemistry (VQE)

Near-term quantum computers (II)

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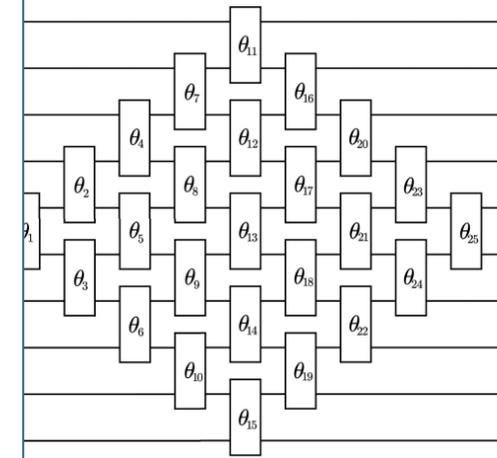
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Quantum Approximate Optimization of Non-Planar Graph Problems
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Hartree-Fock on a superconducting qubit quantum computer

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As the search continues for useful applications of noisy intermediate scale quantum devices, variational simulations of fermionic systems remain one of the most promising directions. Here, we perform a series of quantum simulations of chemistry which involve twice the number of qubits and more than ten times the number of gates as the largest prior experiments. We model the binding energy of H_6 , H_8 , H_{10} and H_{12} chains as well as the isomerization of diazene. We also demonstrate error-mitigation strategies based on N -representability which dramatically improve the effective fidelity of our experiments. Our parameterized ansatz circuits realize the Givens rotation approach to free fermion evolution, which we variationally optimize to prepare the Hartree-Fock wavefunction. This ubiquitous algorithmic primitive corresponds to a rotation of the orbital basis and is required by many proposals for correlated simulations of molecules and Hubbard models. Because free fermion evolutions are classically tractable to simulate, yet still generate highly entangled states over the computational basis, we use these experiments to benchmark the performance of our hardware while establishing a foundation for scaling up more complex correlated quantum simulations of chemistry.

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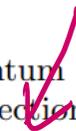
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We demonstrate the use of a quantum approximate optimization algorithm (QAOA) on a connectivity graph model and its application to a non-planar graph problem. Experimental results show that even the largest successful ratio that can be achieved with current hardware is still poor. This behavior is a result of real world hardware limitations on the connectivity of the qubits.

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(VQE)

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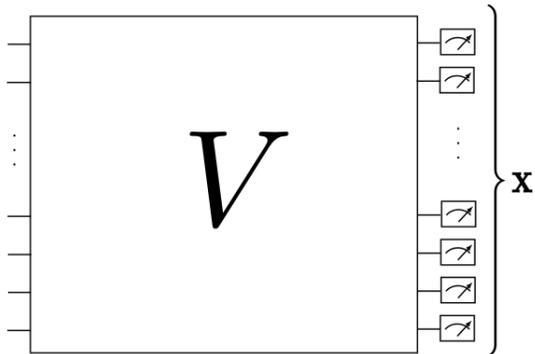
Quantum computational advantage/supremacy

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- Alternative paradigm: engineer (non necessarily practical) problem for which near-term restricted purpose computers could offer potential speedup

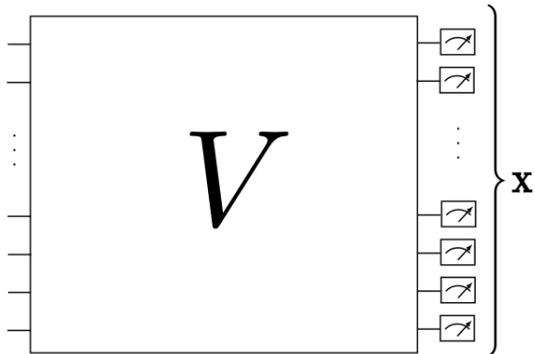
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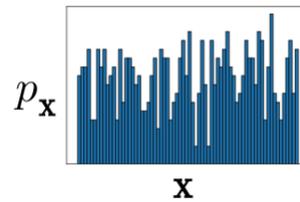


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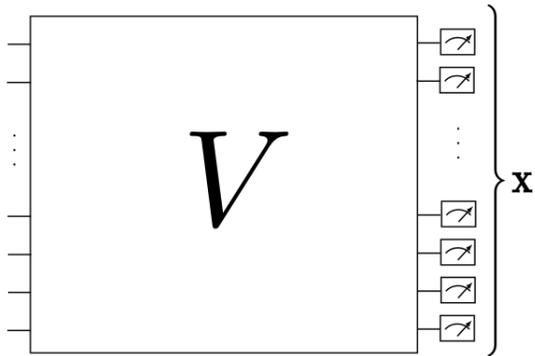


True distribution

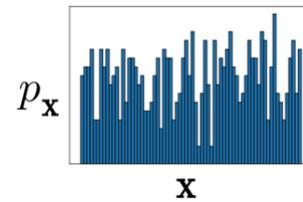


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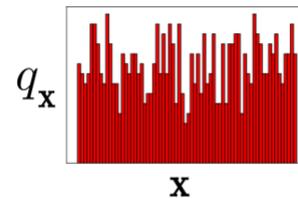
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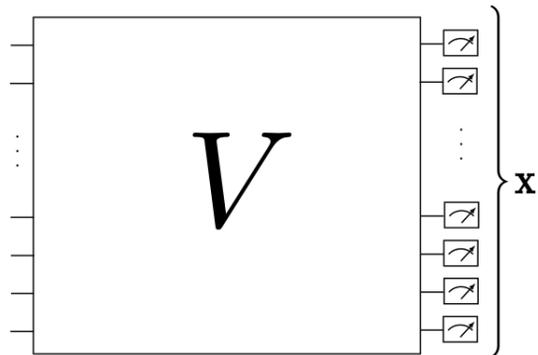


Simulated distribution

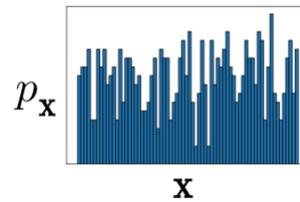


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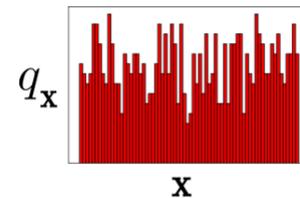
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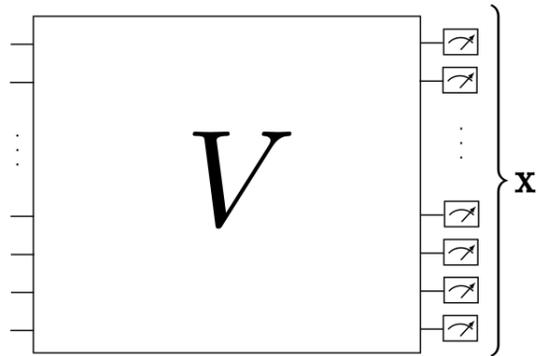


Relative error (\mathbf{R})

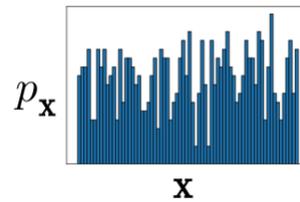
$$\forall \mathbf{x} \quad |p_{\mathbf{x}} - q_{\mathbf{x}}| \leq c p_{\mathbf{x}}$$

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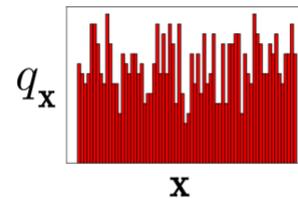
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Relative error (**R**)

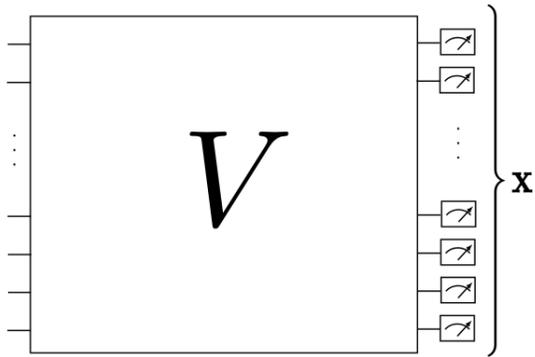
$$\forall \mathbf{x} \quad |p_x - q_x| \leq c p_x$$

Additive error (**A**)

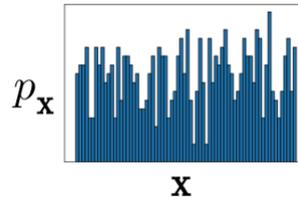
$$TV(\{p_x\}, \{q_x\}) = \frac{1}{2} \sum_x |p_x - q_x|$$

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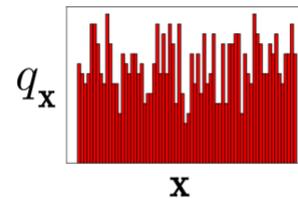
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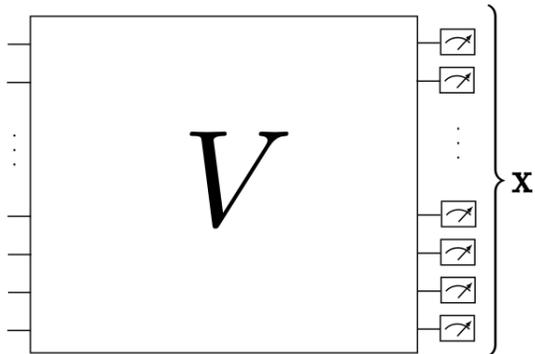
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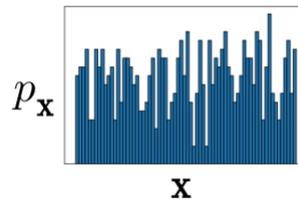
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Quantum computational advantage/supremacy

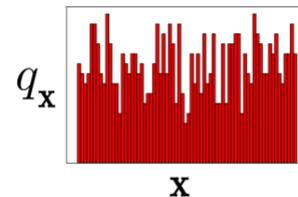
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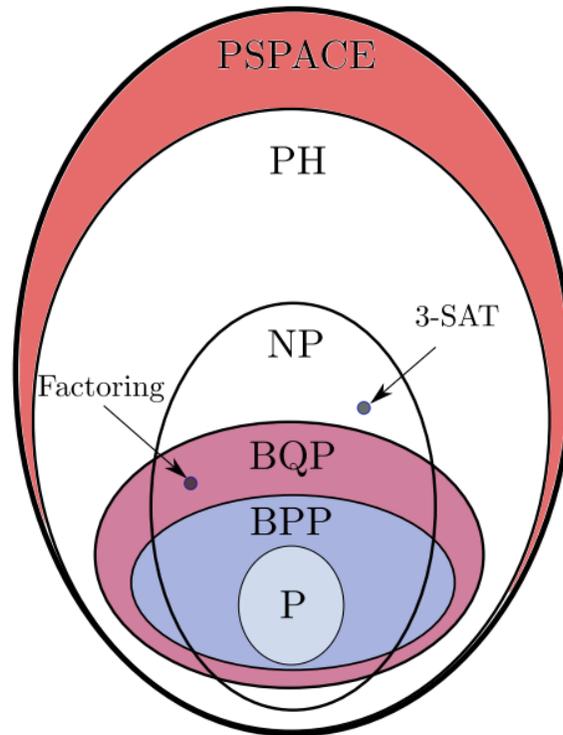
- **Pros:** (in principle) smaller requirements, hardness based on complexity theory
- **Cons:** not practical, noise still affects such proposals

Quantum computational advantage/supremacy (II)

Computer science: polynomial-time computation == efficient

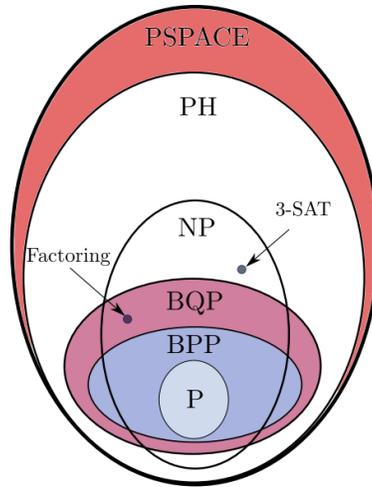
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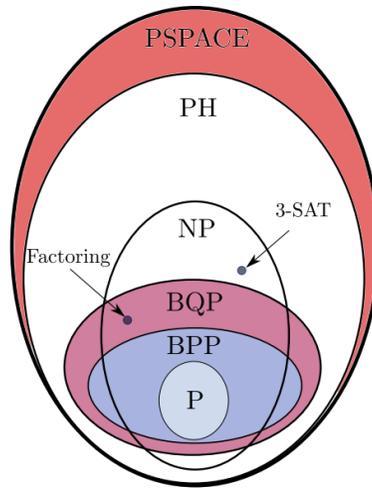
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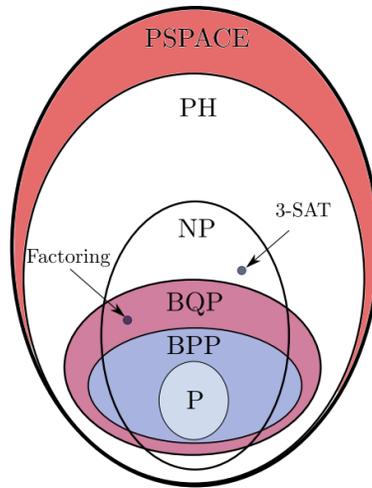
Efficient sampler that, given $V \in \mathcal{E}$, samples \mathbf{x} from $\{q_{\mathbf{x}}(V)\}$ approximating $\{p_{\mathbf{x}}(V)\}$ in R/A error.



Polynomial Hierarchy collapses

Quantum computational advantage/supremacy (II)

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+ conjectures



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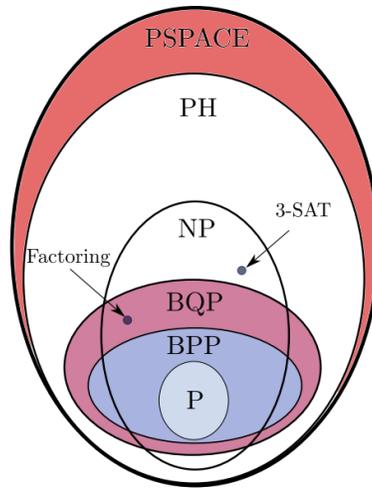
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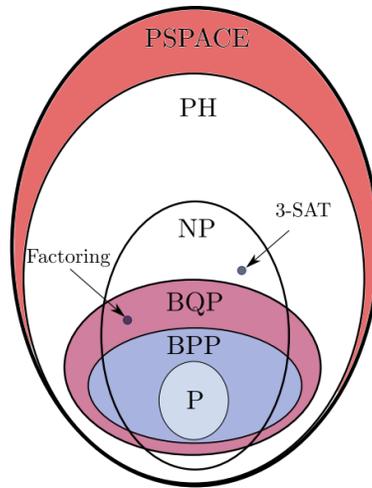


Polynomial Hierarchy collapses

R: Shallow circuits [Terhal-DiVincenzo 2004], IQP [Bremner-Shepard-Jozsa 2010]

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Additive error (**A**)

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Efficient sampler that, given $V \in \mathcal{E}$, samples \mathbf{x} from $\{q_{\mathbf{x}}(V)\}$ approximating $\{p_{\mathbf{x}}(V)\}$ in R/A error.

+ conjectures



Polynomial Hierarchy collapses

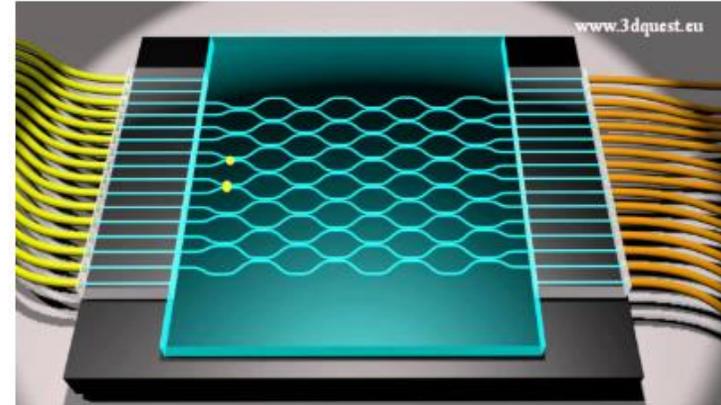
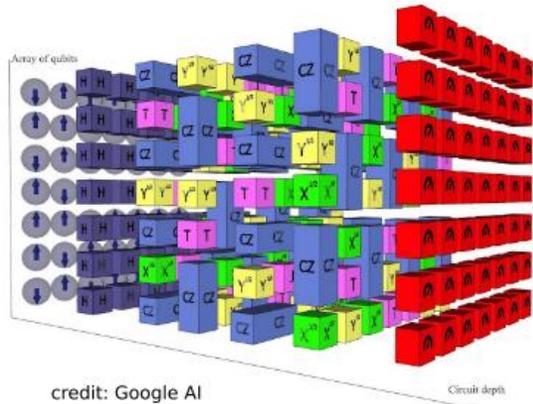
R: Shallow circuits [Terhal-DiVincenzo 2004], IQP [Bremner-Shepard-Jozsa 2010]

A: **Boson Sampling** [Aaronson-Arkhipov 2010], IQP [Bremner-Montanaro-Shepard 2016],

Random Circuit Sampling (RCS) [Boixo et al. 2018] [Bouland et al. 2018] [Movassagh 2019]

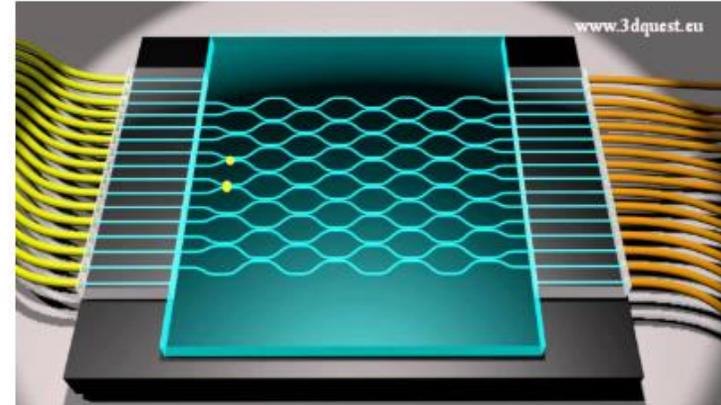
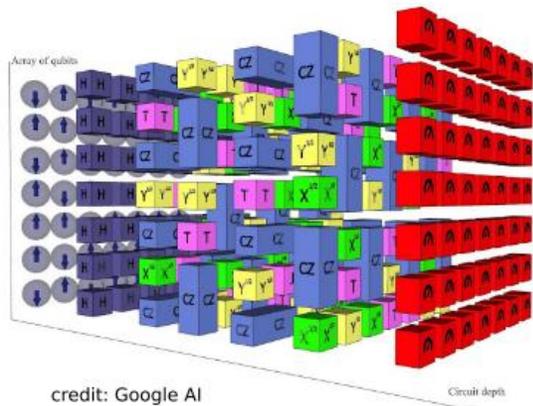
Quantum computational advantage/supremacy (III)

Main experimental platforms: Random Circuit Sampling & Boson Sampling



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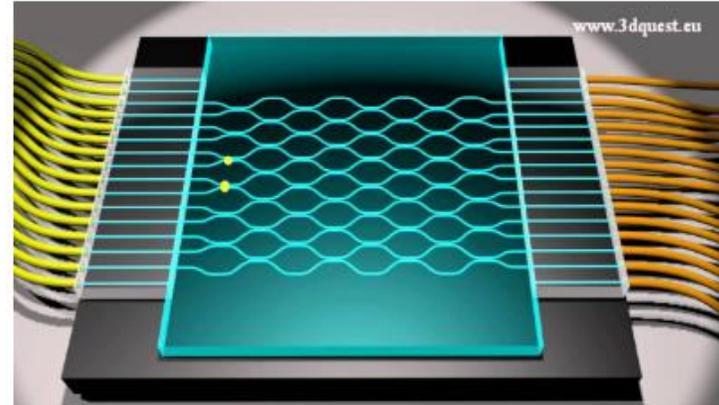
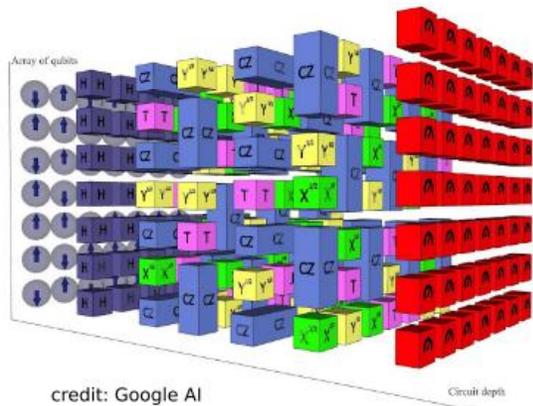
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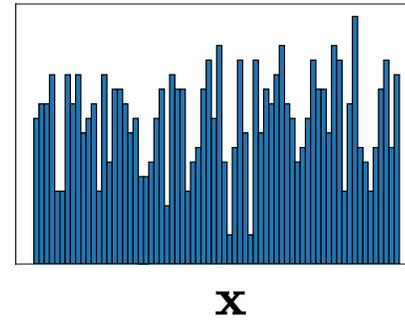
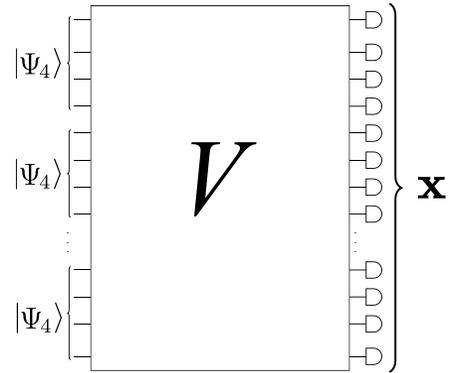


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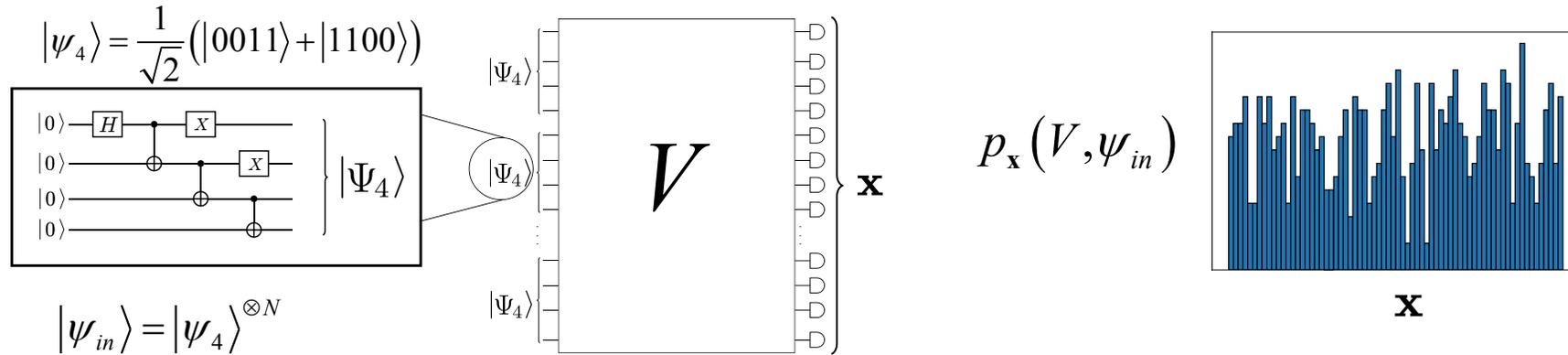
Issues: certification [Hengleiter *et al.* 2019], spoofing by efficient classical simulations [Napp *et al.* 2019] [Renema *et al.* 2018]

Fermion Sampling with magic input states

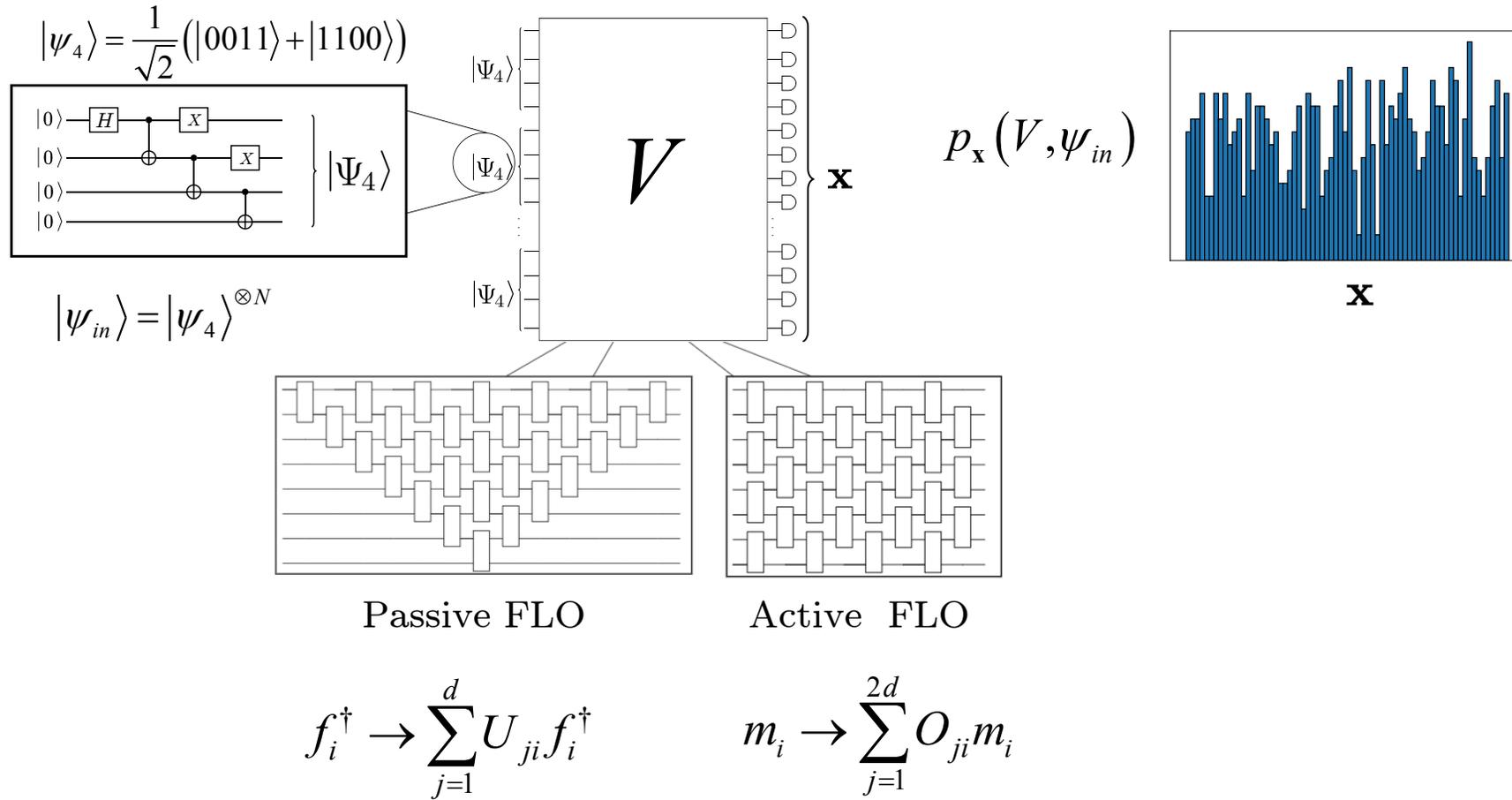
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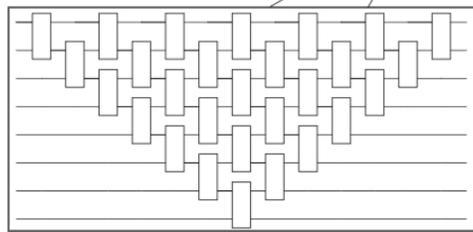
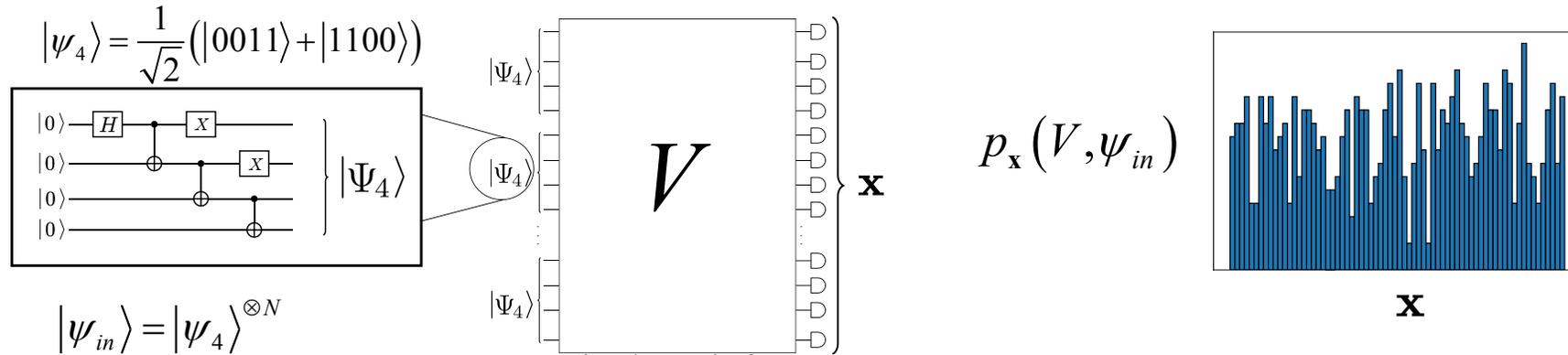
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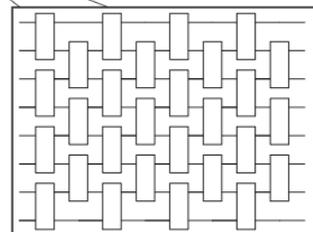


Fermion Sampling with magic input states



Passive FLO

$$f_i^\dagger \rightarrow \sum_{j=1}^d U_{ji} f_i^\dagger$$



Active FLO

$$m_i \rightarrow \sum_{j=1}^{2d} O_{ji} m_i$$

Main results:

- Anticoncentration of $p_{\mathbf{x}}(V, \psi_{in})$
- Average-case #P-hardness of $p_{\mathbf{x}}(V, \psi_{in})$
- Efficient certification of $V \in FLO$

Fermionic linear optics

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Fermionic system of d modes:

$$\mathcal{H} = \bigoplus_{k=0}^d \Lambda^k (\mathbb{C}^d)$$

k -number of fermions

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$$|\mathbf{n}_F\rangle = \prod_{i=1}^d (f_i^\dagger)^{n_i} |0_F\rangle$$

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Active FLO:

$$m_i \rightarrow \sum_{j=1}^{2d} O_{ji} m_j, \quad O \in SO(2d)$$

$$V = \exp\left(\frac{1}{4} \sum_{k,l=1}^{2d} [\log(O)]_{kl} m_k m_l\right)$$

(projective) representation of $SO(2d)$

Jordan-Wigner transformation

Jordan-Wigner transformation

d fermionic modes

d qubits

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Pauli operators

$$\begin{array}{|c|} \hline Z_1 Z_2 \cdots Z_{i-1} X_i \\ \hline Z_1 Z_2 \cdots Z_{i-1} Y_i \\ \hline \end{array}$$

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Particle-number measurements

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Particle-number measurements

Local particle-preserving
quadratic hamiltonians

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Computational basis measurements

Hamiltonians generated by

$$Z_i, Z_{i+1}, X_i Y_{i+1} - Y_i X_{i+1}, X_i X_{i+1} + Y_i Y_{i+1}$$

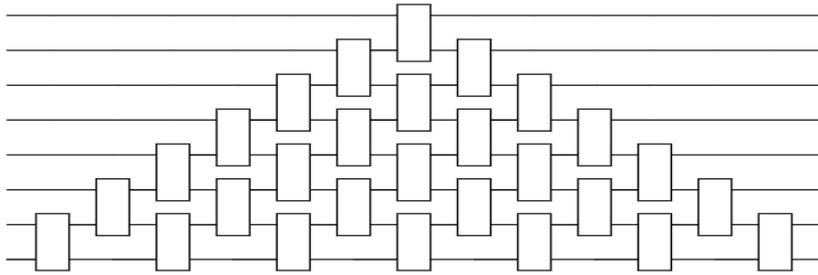
Implementation in superconducting qubits

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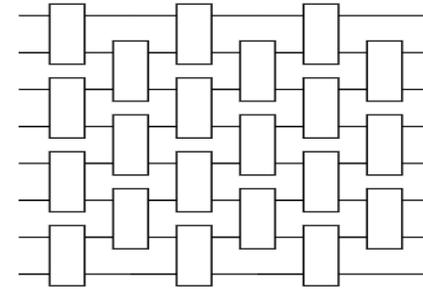
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Implementation in superconducting qubits

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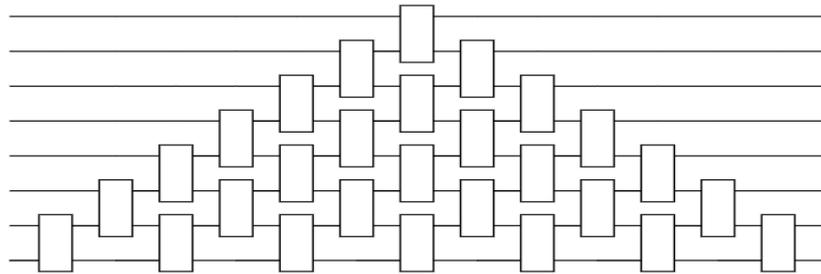
[Reck–Zelinger 1994]



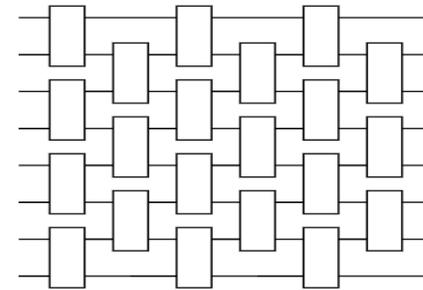
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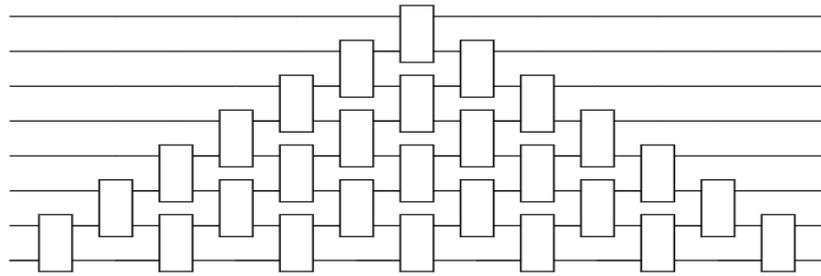


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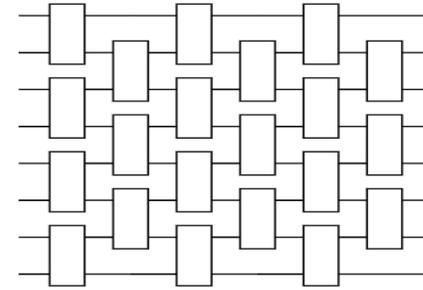
Arbitrary FLO circuit can be realized by circuit of depth $\sim d$ in **1D architecture**

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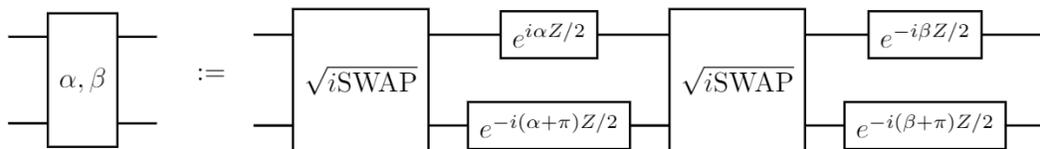
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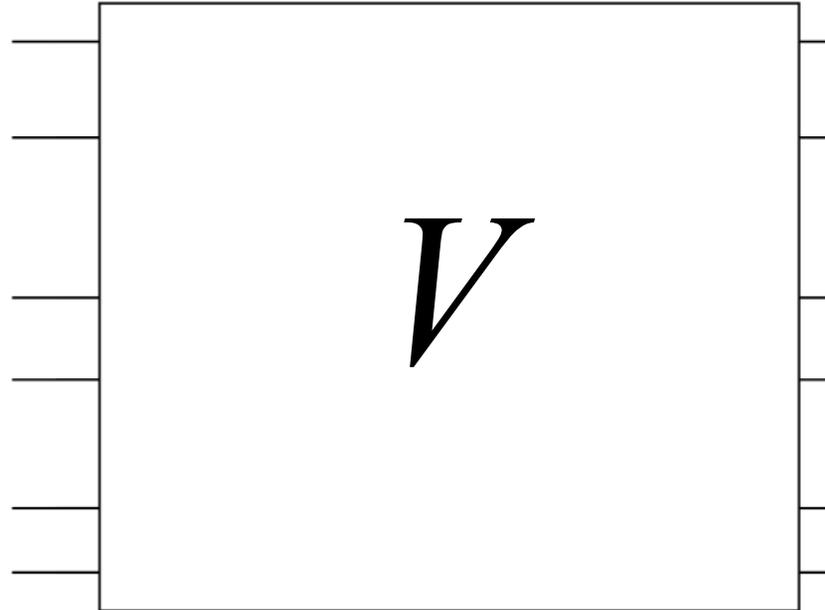
Arbitrary FLO circuit can be realized by circuit of depth $\sim d$ in **1D architecture**

Necessary gates: **native to** superconducting architectures [Arute *et al.* 2020]



$$\sqrt{i\text{SWAP}} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & \frac{1}{\sqrt{2}} & \frac{-i}{\sqrt{2}} & 0 \\ 0 & \frac{-i}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}$$

Efficient tomography of FLO circuits



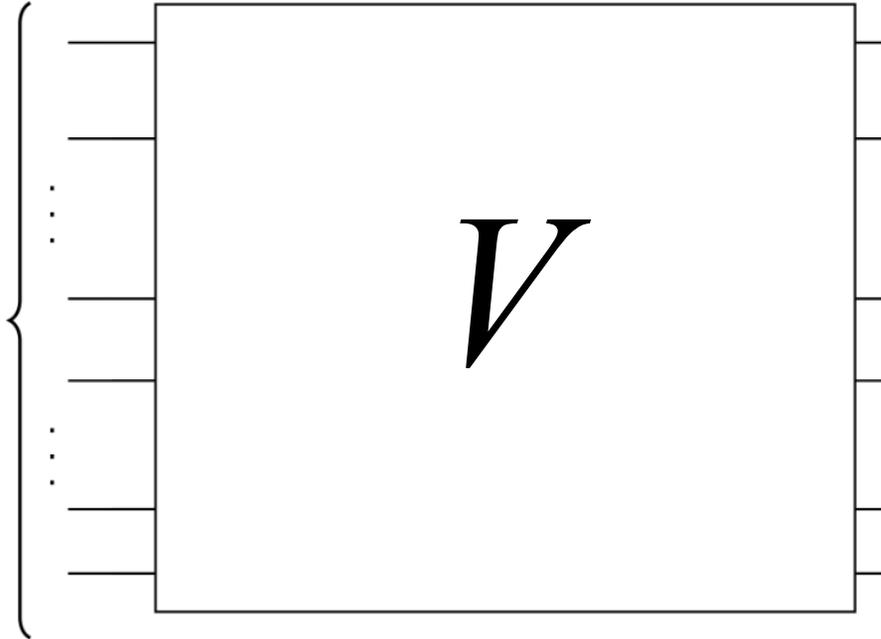
Efficient tomography of FLO circuits

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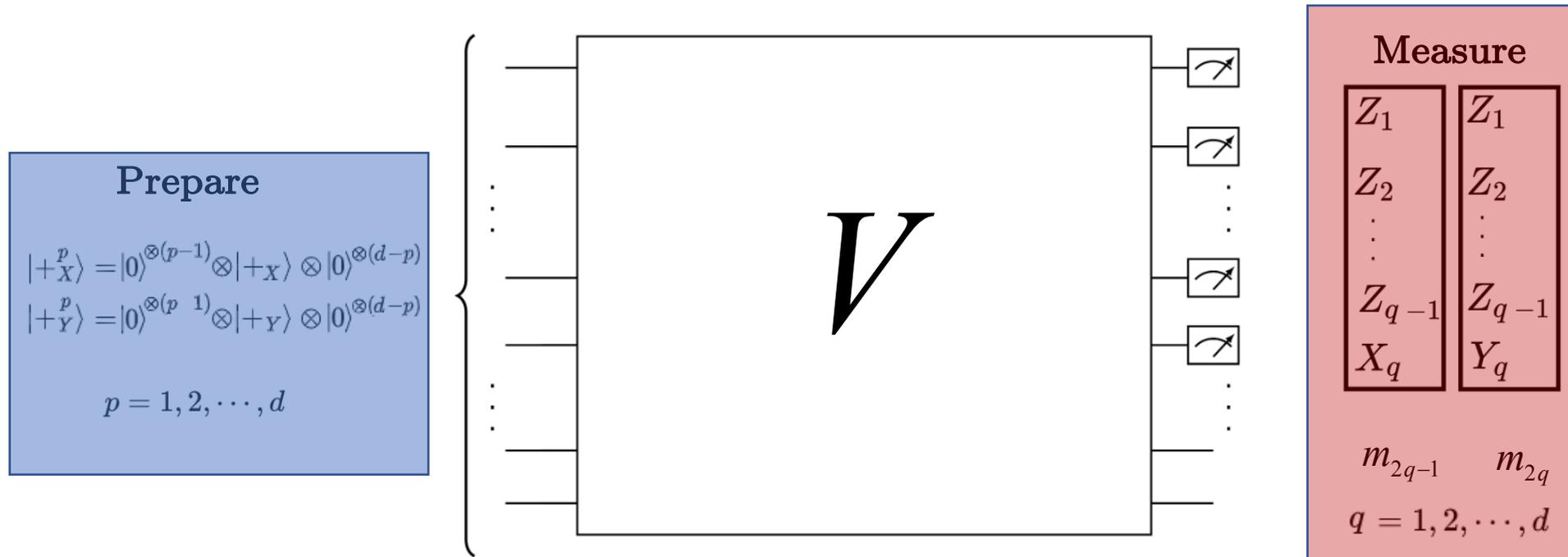
$$|+_X^p\rangle = |0\rangle^{\otimes(p-1)} \otimes |+_X\rangle \otimes |0\rangle^{\otimes(d-p)}$$

$$|+_Y^p\rangle = |0\rangle^{\otimes(p-1)} \otimes |+_Y\rangle \otimes |0\rangle^{\otimes(d-p)}$$

$$p = 1, 2, \dots, d$$



Efficient tomography of FLO circuits

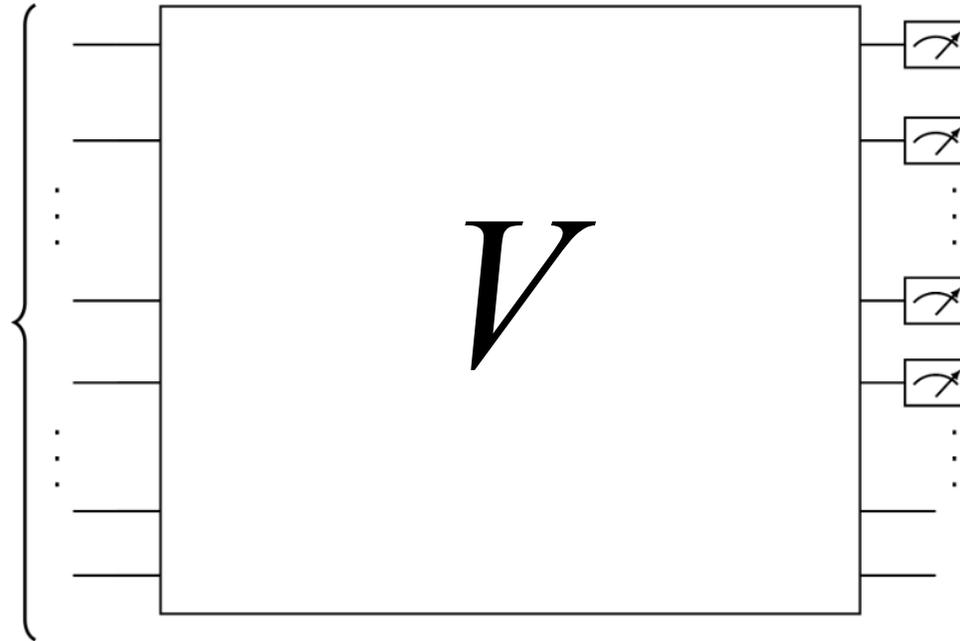


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Measure

Z_1	Z_1
Z_2	Z_2
\vdots	\vdots
Z_{q-1}	Z_{q-1}
X_q	Y_q

m_{2q-1} m_{2q}

$q = 1, 2, \dots, d$

Collect statistics

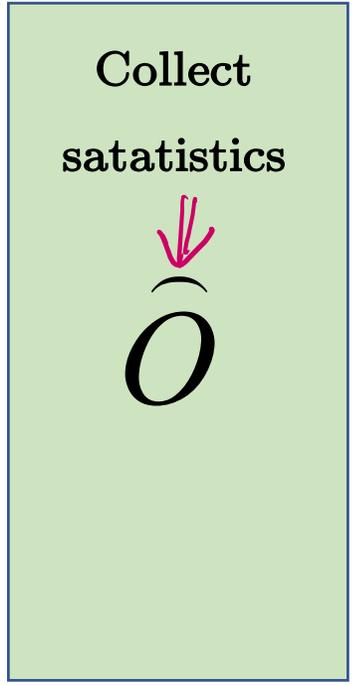
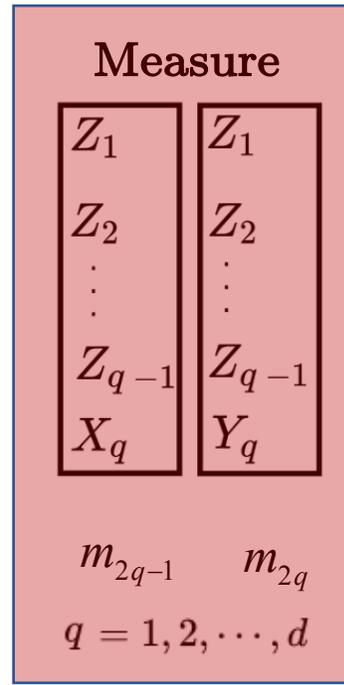
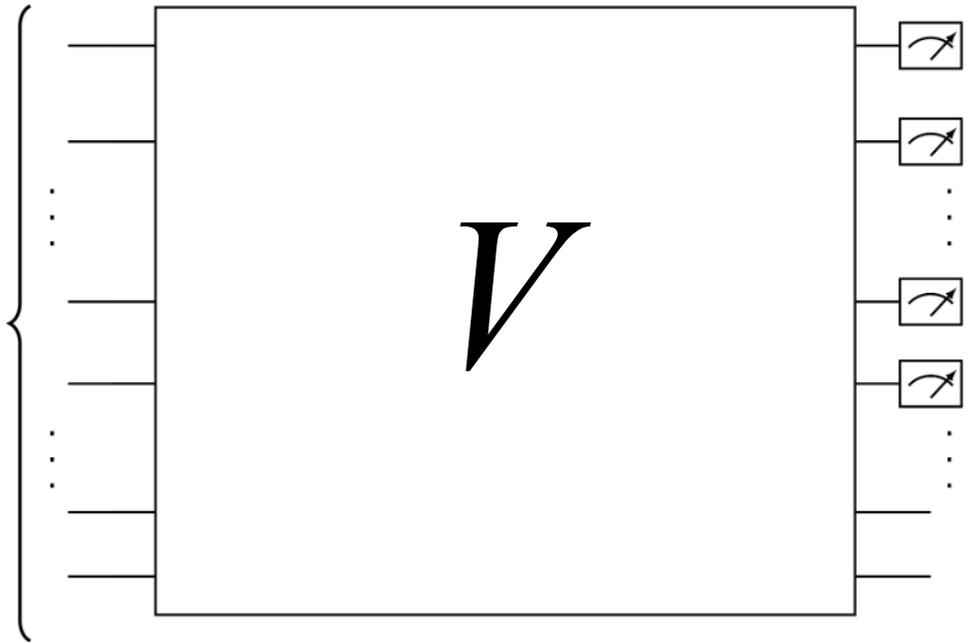
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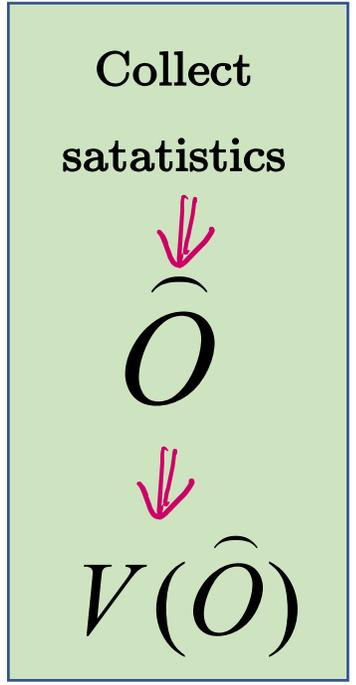
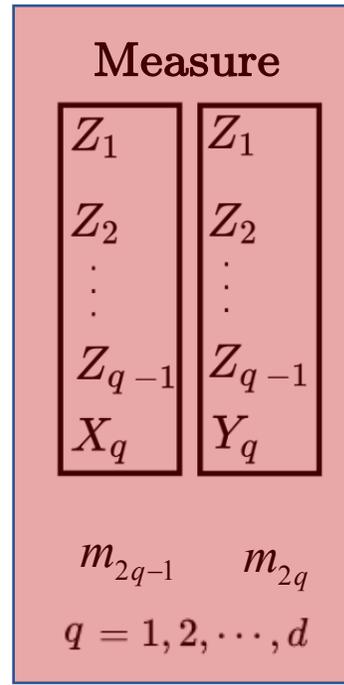
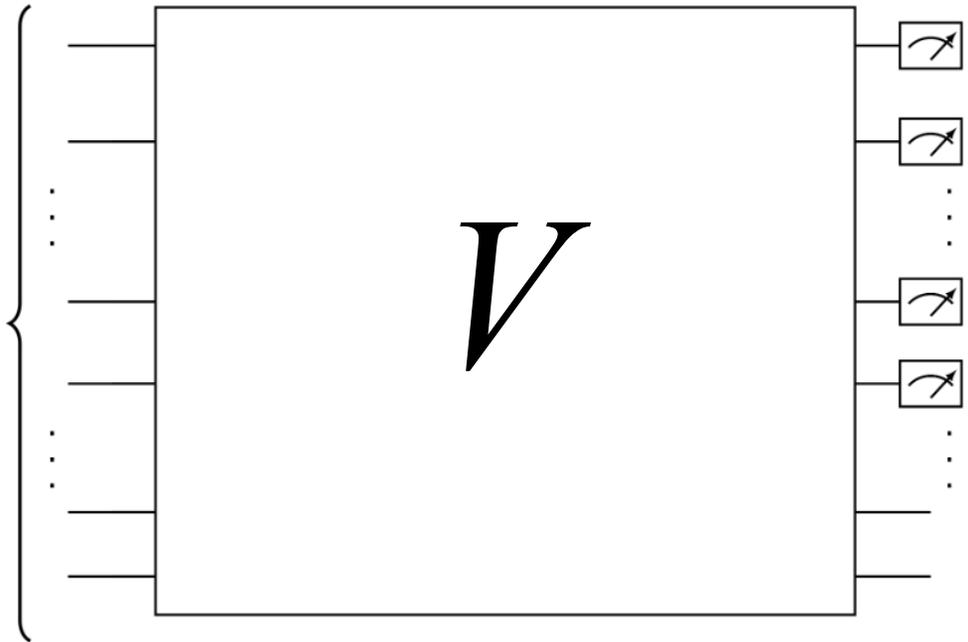
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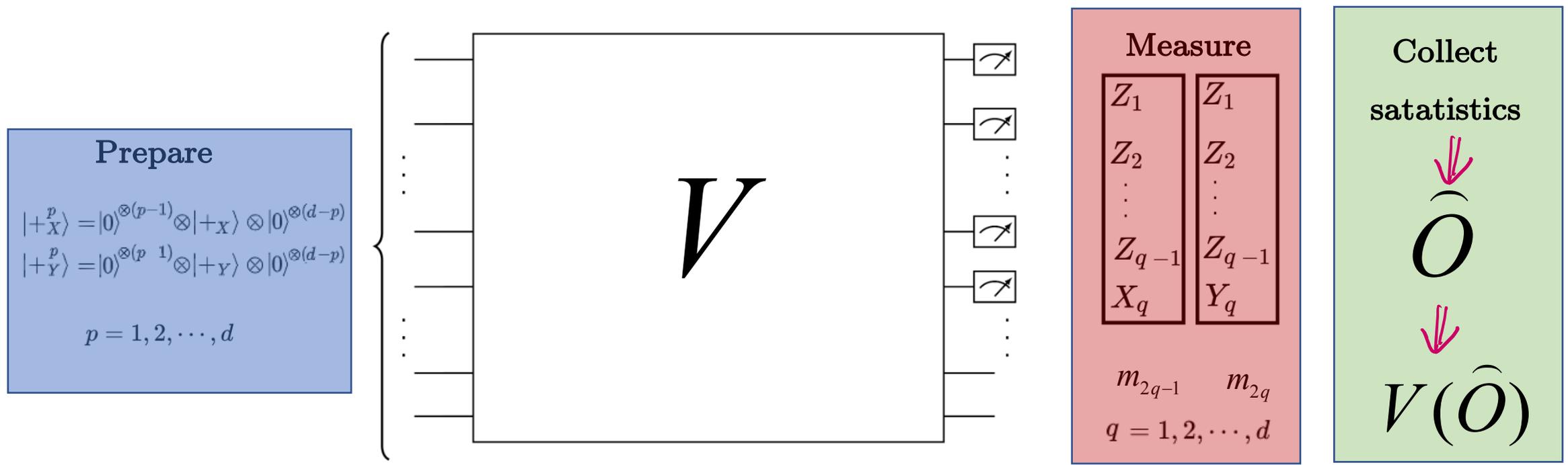
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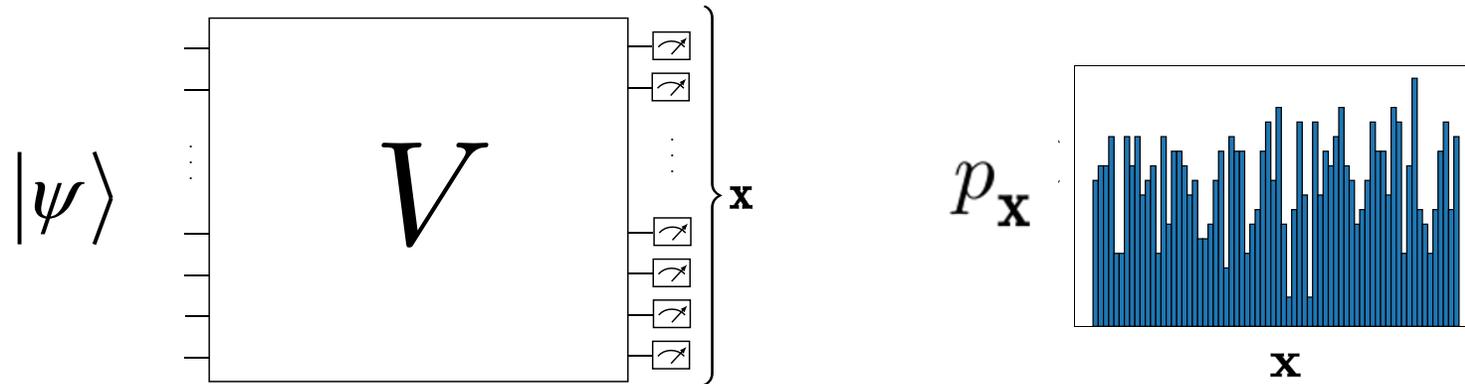


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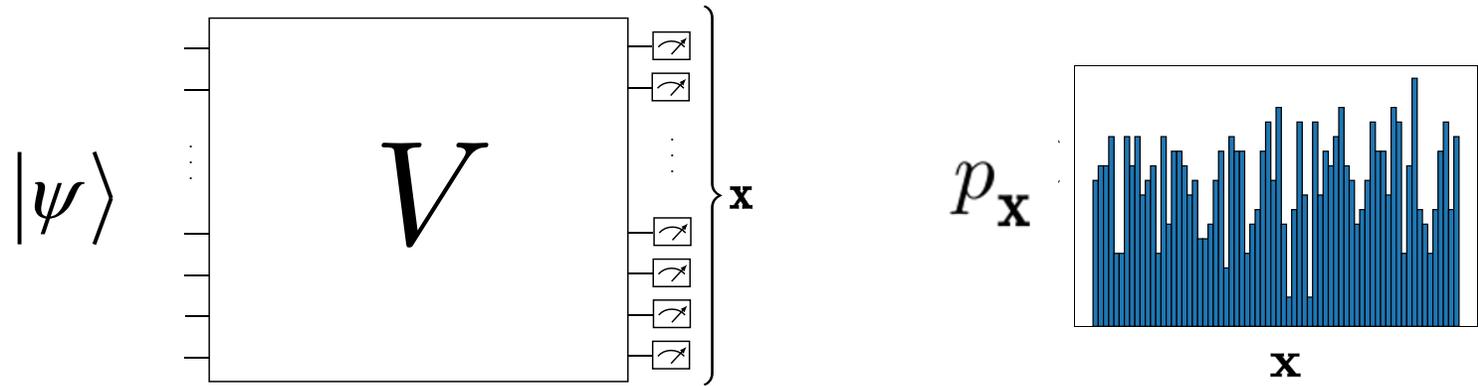


Result: If $V \in FLO$, then the above scheme gives an estimate $V(\hat{O})$ such that $\|V - V(\hat{O})\|_{\diamond} \leq \varepsilon$ using $O\left(\frac{d^3}{\varepsilon^2}\right)$ measurement rounds.

Hardness of Fermion Sampling

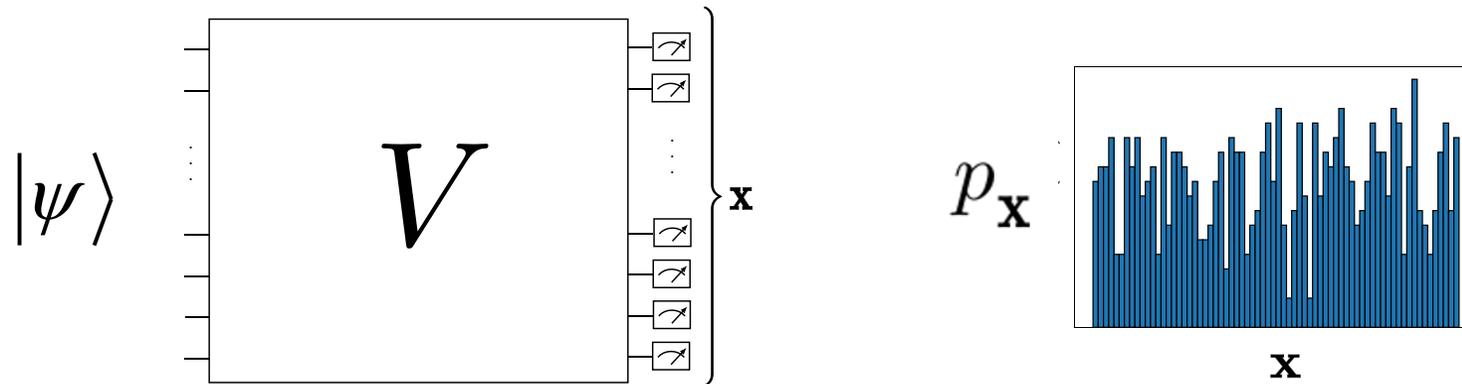


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If $|\psi\rangle$ is free (fermionic Gaussian or Slater determinant), then **sampling is classically easy** [Valiant 2000] [Terhal-DiVincenzo 2001] [Jozsa-Miyake 2008]

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Striking difference between Fermion Sampling and Boson Sampling!

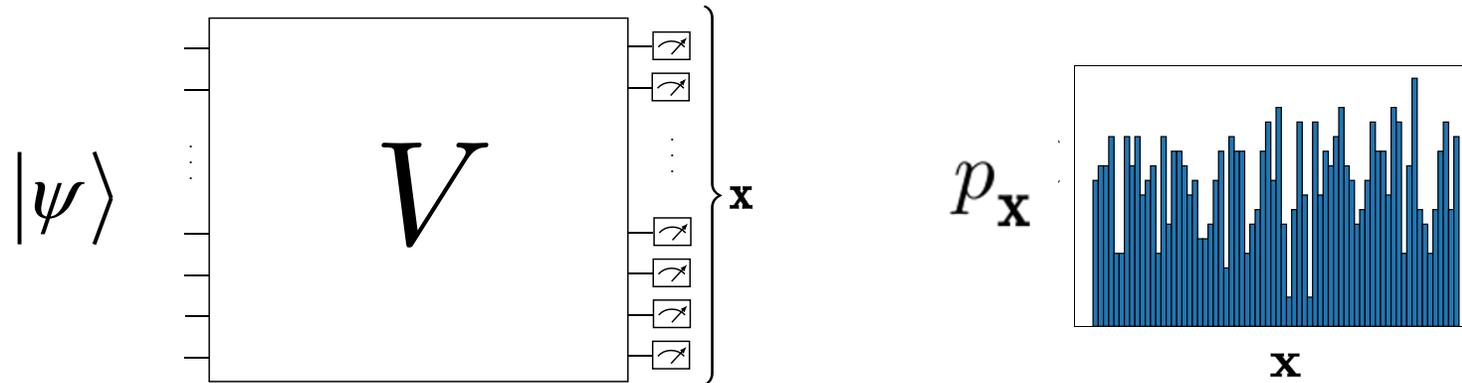
$$p_x^{bos} \propto |Per(U_x)|^2$$

$$p_x^{fer} \propto |Det(U_x)|^2$$

$$Per(A) = \sum_{\sigma \in S_n} \prod_{i=1}^n A_{\sigma(i),i}$$

$$Det(A) = \sum_{\sigma \in S_n} \text{sgn}(\sigma) \prod_{i=1}^n A_{\sigma(i),i}$$

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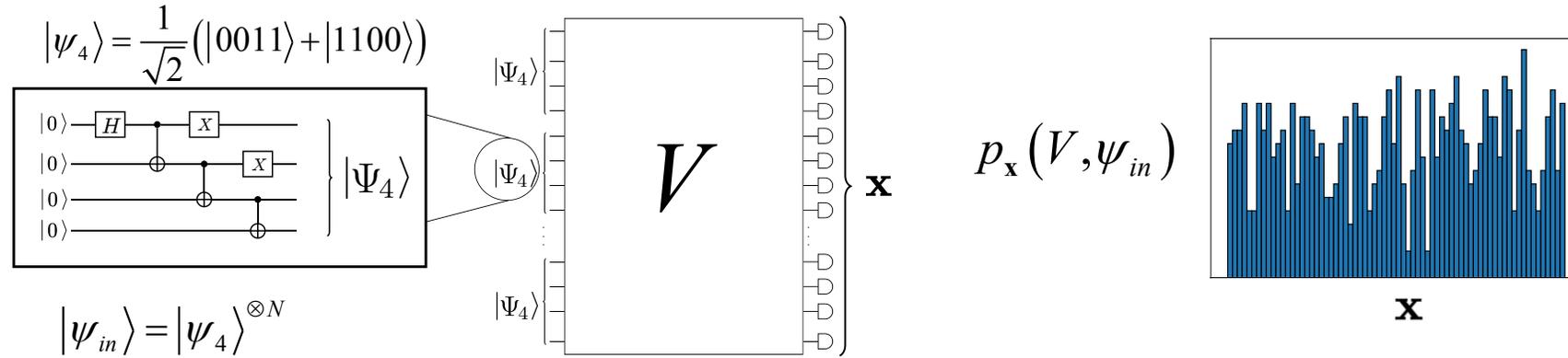
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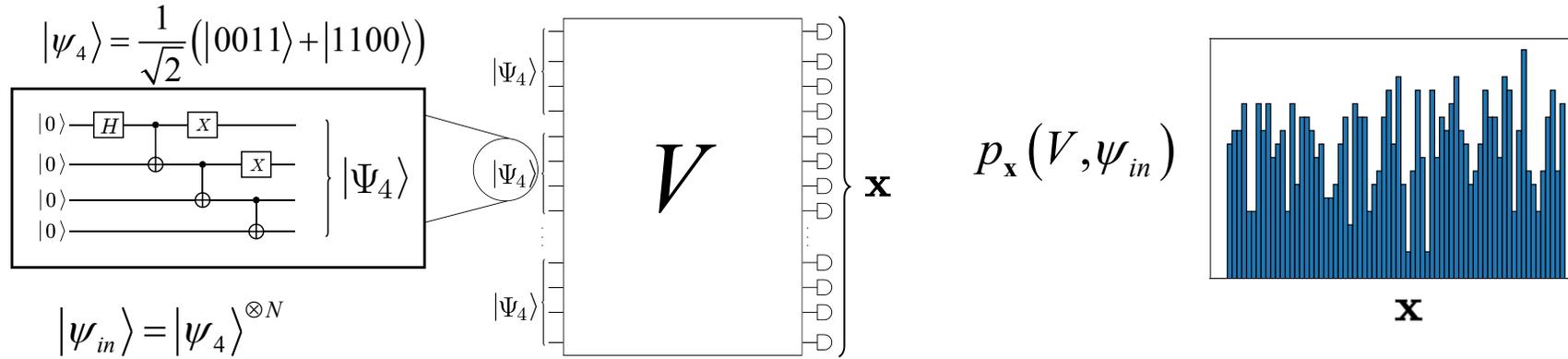
Avi Wigderson

Determinant vs Permanent dichotomy in complexity theory (#P -hardness of Per !)

Hardness of Fermion Sampling (II)

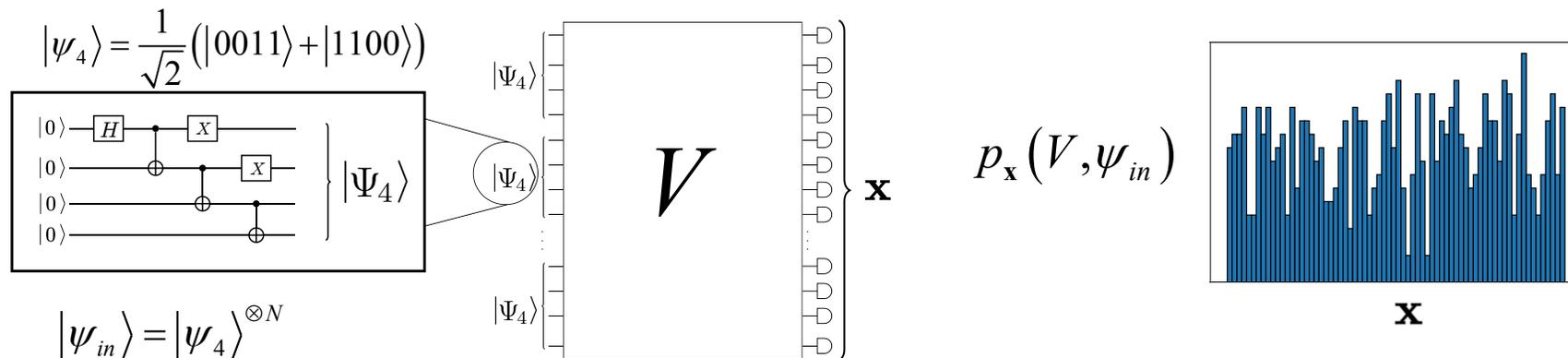


Hardness of Fermion Sampling (II)



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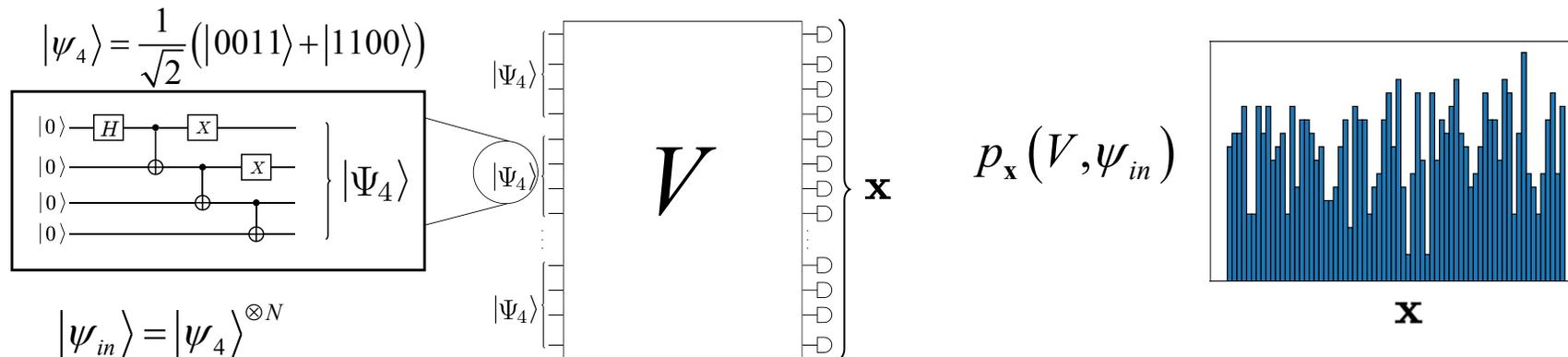
$$p_{\mathbf{x}}(V(U), \psi_{in}) \propto |D_{2,2}(U_{\mathbf{x}})|^2$$

$$D_{2,2}(U_{\mathbf{x}}) = \sum_{\mathbf{y}} \text{Det}(U_{\mathbf{x},\mathbf{y}})$$



Dimitri Ivanov

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Dimitri Ivanov

Mixed discriminants are #P-hard to compute.

Alternatively, $|\psi_4\rangle$ promote active FLO to universality [Bravyi 2006] [Hebenstreit *et al.* 2019]

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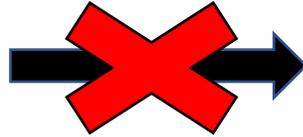
Hardness of \mathbf{A} -approximate sampling
from $\{p_x(V)\}$ for $V \sim \mathcal{E}$

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$$TV(\{p_x\}, \{q_x\}) = \frac{1}{2} \sum_x |p_x - q_x|$$

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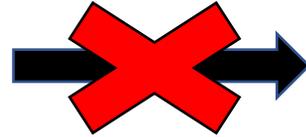
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from $\{p_x(V)\}$ for $V \sim \mathcal{E}$

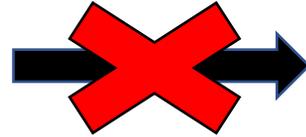
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Additive error (\mathbf{A})

$$TV(\{p_x\}, \{q_x\}) = \frac{1}{2} \sum_x |p_x - q_x|$$

Hardness of Fermion Sampling (III)

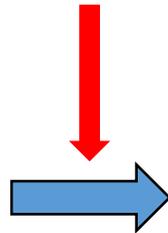
Hardness of computation $p_{x_0}(V_0)$
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Approximation of $p_{x_0}(V)$
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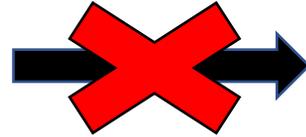


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Holds for **RQC** [Movassagh 2019], [Bouland *et al.* 2018]

Hardness of Fermion Sampling (IV)

Hardness of computation $p_{x_0}(V_0, \psi_{in})$
for fixed $V_0 \in FLO$



Hardness of \mathbf{A} -approximate sampling
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Anticoncentration for FLO circuits

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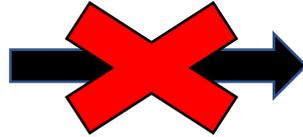
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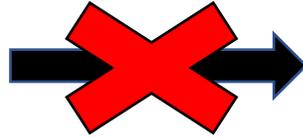
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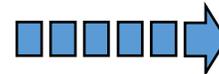
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Result: average-case hardness of approximation
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Conjecture: average-case hardness
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Result: There exist a constant $C > 0$ such that for any $0 < \alpha < 1$

$$\Pr_{V \sim \mu} \left[p_{\mathbf{x}_0}(V, \Psi_{in}) > \frac{\alpha}{|\mathcal{H}|} \right] > (1 - \alpha)^2 C$$

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Numerics suggests that for Gaussian μ probabilities $p_{\mathbf{x}}(V, \psi)$ **do not anticoncentrate**.

Anticoncentration for Fermion Sampling (II)

Proof sketch:

- Paley-Zygmund $\Pr (X > \alpha \mathbb{E} X) \geq (1 - \alpha)^2 \frac{(\mathbb{E} X)^2}{\mathbb{E} X^2}$, $\alpha \in [0, 1]$

- We set $X_v = p_{x_0}(v, \Psi_{in}) = \text{tr}(|x_0\rangle\langle x_0| \Pi(v) \Psi_{in} \Pi(v^\dagger))$, $\Pi: G \rightarrow U(\mathcal{H})$ suitable irrep of G

- $\mathbb{E}_{v \sim \mu} X_v = \frac{1}{|\mathcal{H}|}$

- $\mathbb{E}_{v \sim \mu} X_v^2 = \int_G d\mu(v) \text{tr}(\Pi(v)^{\otimes 2} |x_0\rangle\langle x_0|^{\otimes 2} \Pi(v^\dagger)^{\otimes 2} \Psi_{in}^{\otimes 2}) = \frac{1}{|\tilde{\mathcal{H}}|} \text{tr}(|P_{\tilde{\mathcal{H}}}\rangle\langle P_{\tilde{\mathcal{H}}}| \Psi_{in}^{\otimes 2})$

- Inserting to P-Z: $\Pr_{v \sim \mu} (p_{x_0}(v, \Psi_{in}) \geq \alpha \frac{1}{|\mathcal{H}|}) \geq (1 - \alpha)^2 \frac{|\tilde{\mathcal{H}}|^2}{|\mathcal{H}|^2} \frac{1}{\text{tr}(|P_{\tilde{\mathcal{H}}}\rangle\langle P_{\tilde{\mathcal{H}}}| \Psi_{in}^{\otimes 2})} = O\left(\frac{1}{N^\alpha}\right)$

$\sim \frac{1}{N^\alpha}$ CONSTANT

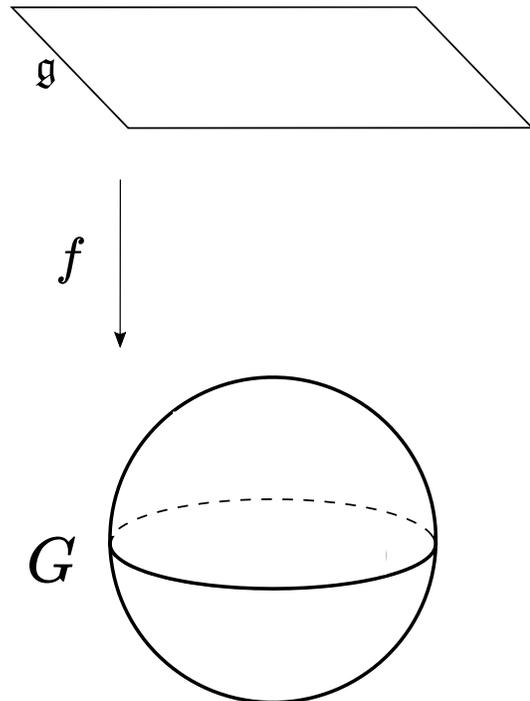
Average-case hardness

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- **Goal:** construct a low-degree rational interpolation between a #P-hard FLO circuit and generic circuits
- Use polynomial interpolation technique to recover the value of the worst-case probability from those of generic circuits
- To achieve the goal, we use the **Cayley-path** deformation [Movassagh 2019]

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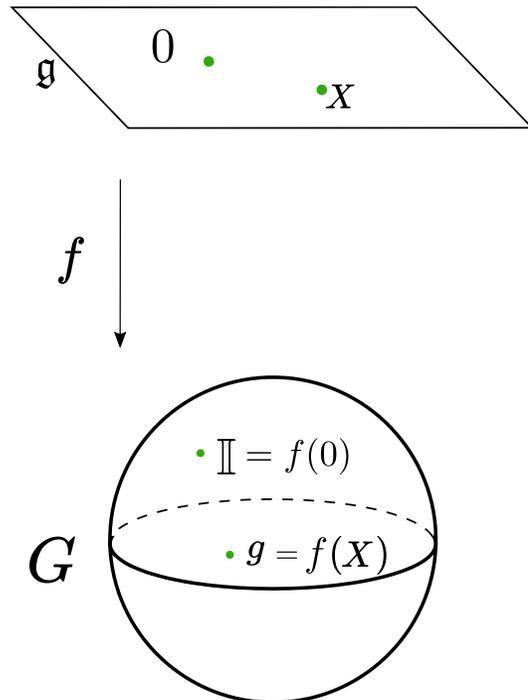


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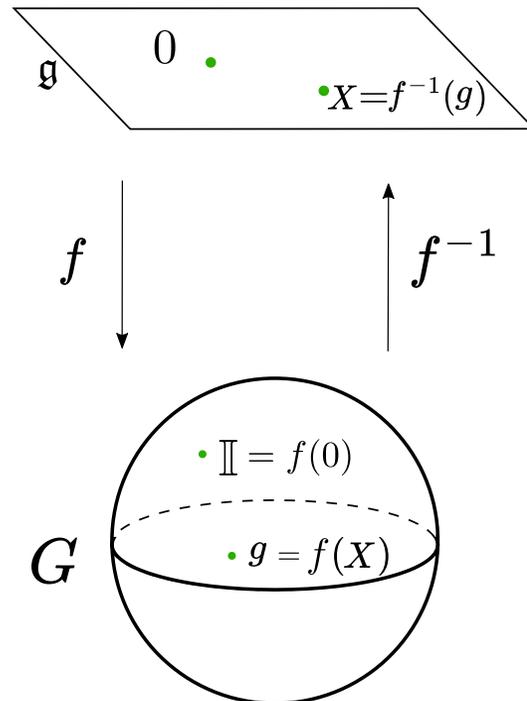


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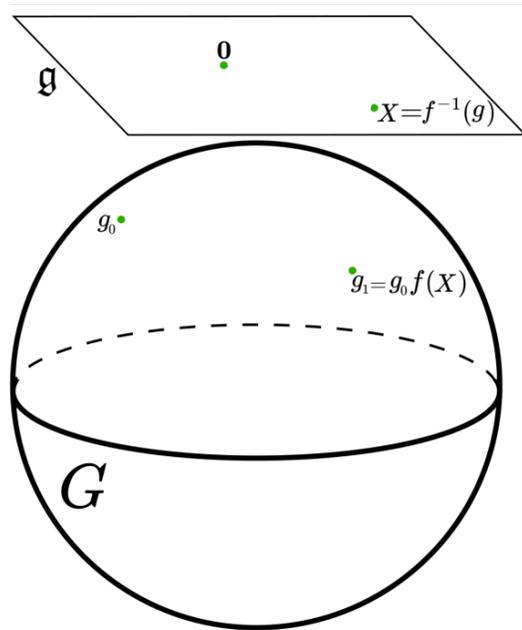
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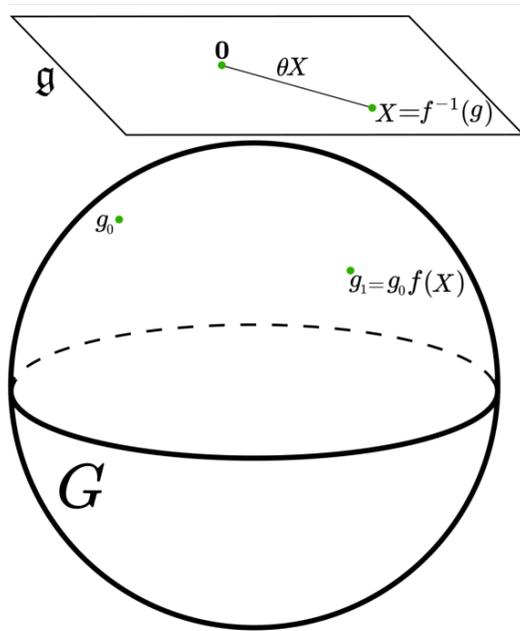
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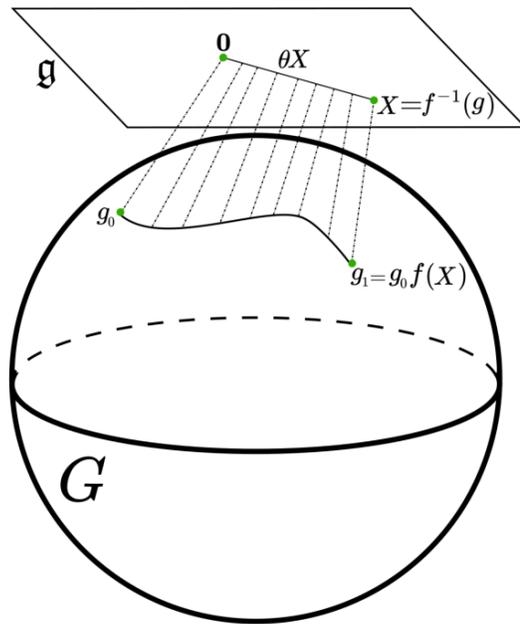
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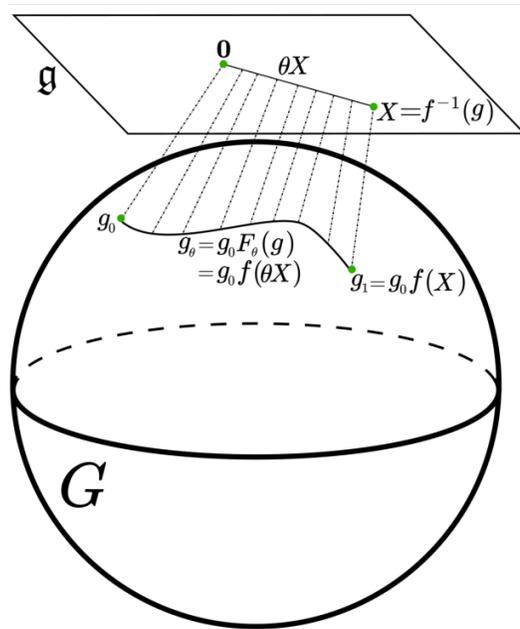
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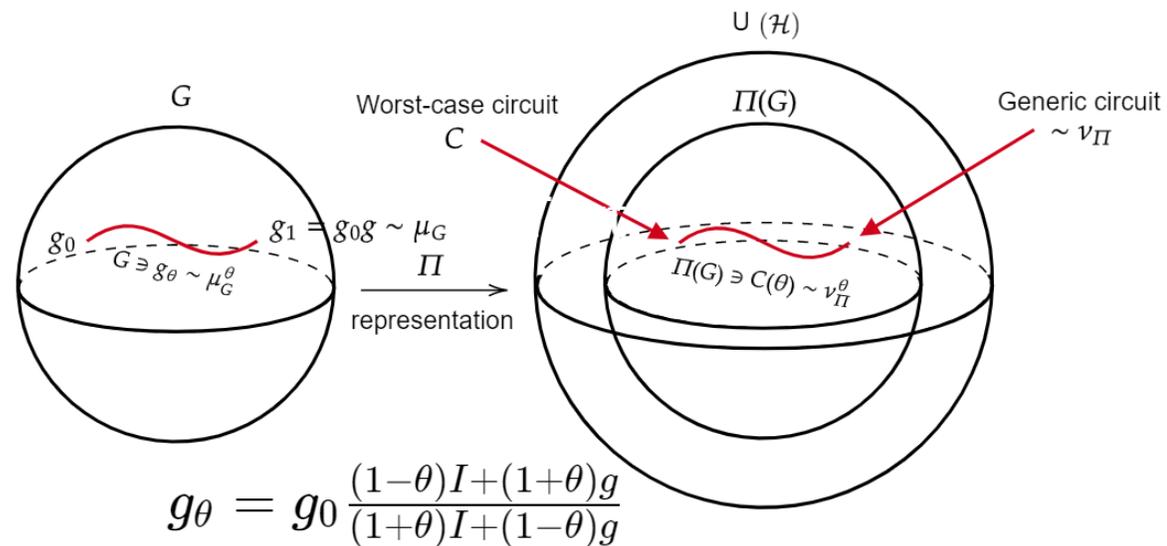
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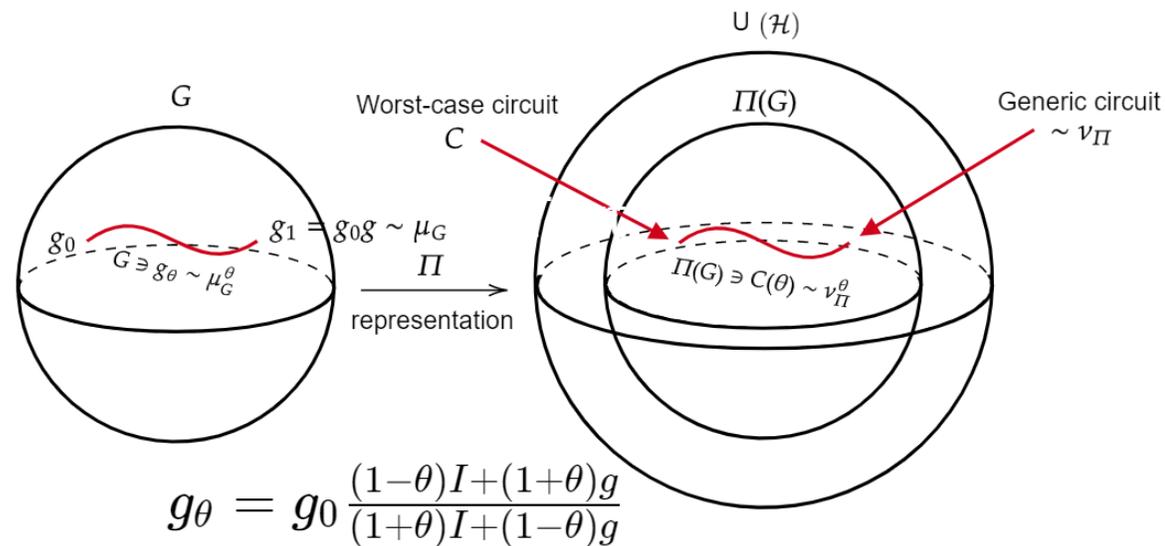
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Difference to previous work: instead of deforming individual gates, we deform at the level of the symmetry group, which is represented as a global circuit.

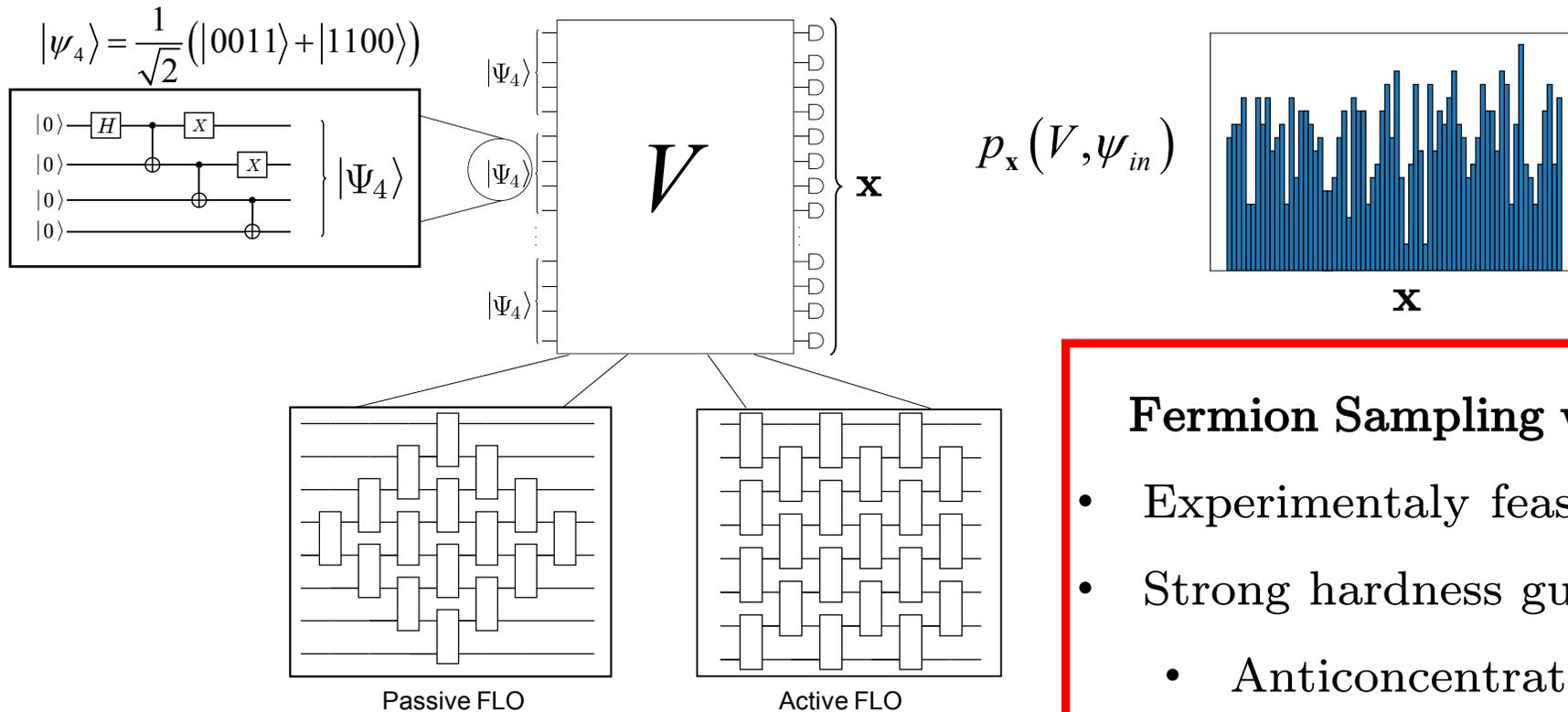
Average-case hardness (II)

Result: It is #P-hard to compute values of $p_{x_0}(V, \Psi_{in})$ with probability greater than $\frac{3}{4} + \frac{1}{\text{poly}N}$ over the choice of $V \sim \mu$

Result: It is #P-hard to approximate probability $p_{x_0}(V, \Psi_{in})$ to within accuracy $\epsilon = \exp(-\Theta(N^6))$ with probability greater than $1 - o(N^{-2})$ over the choice of $V \sim \mu$

- **Movassagh's result:** $\epsilon = \exp(-\Theta(N^{4.5}))$ for the Google's layout
- **Supremacy conjecture:** constant relative error with constant probability over the choice of $V \sim \mu$

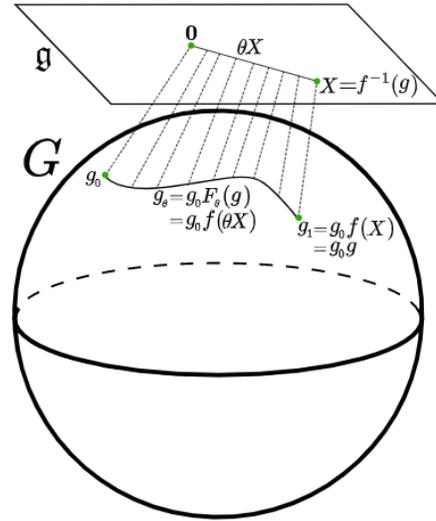
Conclusions



Fermion Sampling with magic input states

- Experimentally feasible
- Strong hardness guarantees
 - Anticoncentration of $p_x(V, \psi_{in})$
 - Average case hardness of $p_x(V, \psi_{in})$
- FLO unitaries can be efficiently certified

Outlook and open problems

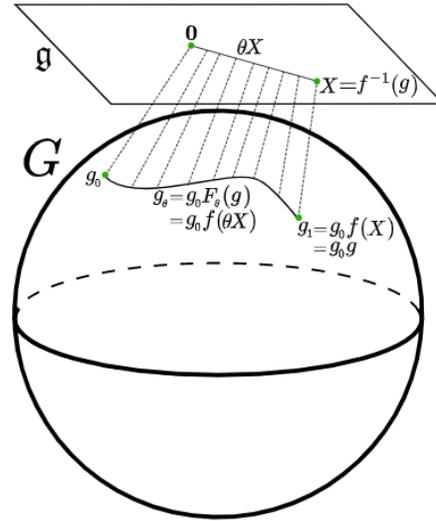


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- Verification and certification of Fermion Sampling
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Thank you!

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